



Third Semester LL.M. Examination, June/July 2012
LAW OF DESIGNS, INTEGRATED CIRCUITS, GEOGRAPHICAL
INDICATIONS, CONFIDENTIAL INFORMATION AND
TRADITIONAL KNOWLEDGE
Specialization Paper – 5
(IPR Branch)

Duration : 3 Hours

Max. Marks : 80

- Instructions:** 1. Answer **all** questions.
2. Figures to the **right** indicate **marks**.

- Q. No. 1. (a) Discuss the procedure for Registration of geographical indication in India. Explain the conditions for Registration. Marks : 10
- OR
- Explain the concept of industrial design as intellectual property.
- (b) What is semiconductor integrated circuit ? Marks : 6
- OR
- What do you mean by 'Copyright in Registered Designs' ?
- Q. No. 2. (a) Explain the national legal framework for the protection of confidential information. Marks : 10
- OR
- Discuss the establishment, powers and functions of Lay-out Designs Appellate Board.
- (b) Who is a producer under Geographical Indications Act ? Marks : 6
- OR
- What do you mean by piracy of registered design ?

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Q. No. 3. (a) Discuss the grounds on which the registration of industrial design may be refused.

Marks : 10

OR

What is false or deceptive indication of sources on goods ?
Explain their concept with reference to Madrid Agreement.

(b) What is Topography ? Explain .

Marks : 6

OR

What are the essential ingredients of confidential information ?

Q. No. 4. (a) Examine the provisions relating to assignment and transmission of rights in lay-out designs.

Marks : 10

OR

Explain the provisions of TRIPS relating to industrial designs.

(b) Distinguish between 'author of design' and 'proprietor of design'. Marks : 6

OR

Write a note on 'Jointly owned lay-out design'.

Q. No. 5. (a) Explain the features of Paris convention in relation to industrial designs.

Marks : 10

OR

Examine the provisions of Lisbon Agreement for the protection of appellations of origin.

(b) What is National Treatment ? Explain.

Marks : 6

OR

Explain the meaning of 'goods' under Geographical Indications Act.