

General Certificate of Secondary Education

Electronics

Unit 1 Written Paper

Specimen Mark Scheme

1	(a)	Any five dangers from: working alone working on mains powered circuit circuit live/plugged in no earth capacitor charged across mains incorrectly polarised water pear mains supply	
		soldering iron danger $\sqrt{\sqrt{\sqrt{\sqrt{\sqrt{\sqrt{\sqrt{\sqrt{\sqrt{\sqrt{\sqrt{\sqrt{\sqrt{\sqrt{\sqrt{\sqrt{\sqrt{\sqrt{$	(5 marks)
1	(b)	shock related effect✓ burn related effect✓	(2 marks)
1	(C)	remove victim from mains \checkmark put in recovery position \checkmark resident formation respectively (max 3)	suscitation✓
			(3 marks)

Total Mark: 10

Question 2

2	(a)	(i)	temperature	sensor√	(1 mark)
2	(a)	(ii)	heater√		(1 mark)
2	(a)	(iii)	comparator√		(1 mark)
2	(b)	(i)	comparator√		(1
2	(b)	(ii)	temperature	sensor√	(1 mark)
					(1 mark)
2	(c)	transi	istor switch√	electromagnetic relay√ (any order)	(2 marks)
2	(d)	comparator output goes high (or changes) ✓ transistor switches relay ✓ relay switches on ✓ heater switches on ✓ (max 3)		(3 marks)	

Total Mark: 10

AQA

3 (a) OR✓ AND✓ NAND✓

3 (b)

А	В	С	D	Q
0	0	0	1	0
0	1	1 ✓	1 ✓	1
1	0	1	1	1
1	1	1	0	0
		 ✓ 	· ·	· ·

(6 marks)

(3 marks)



(1 mark)

Total Mark: 10

Question 4

4	(a)	capacitor✓ 470 microfarads✓ 16V✓ polarity✓	(4 marks)
4	(b)	resistor✓ 47√000√Ω√ 5%√ to limit current√	(6 marks)

Total Mark: 10



5	(a)	7 ∨ ✓	(1 mark)
5	(b)	R = V/I = 7/0.2 ✓ = $350 ✓ \Omega ✓$ (2 max)	(2 marks)
5	(C)	360 Ω✓	(1 mark)
5	(d)	orange ✓ blue✓ brown✓ gold✓	(4 marks)

Total Mark: 8

Question 6

6	(a)	CD pla Amplif	ayer in correct position \checkmark , Tuner in correct position \checkmark , ier in correct position \checkmark ,	
				(4 marks)
6	(b)	(i)	tuned circuit✓ selects✓ (one) frequency✓	(3 marks)
6	(b)	(ii)	demodulator \checkmark separates \checkmark the signal from the carrier wave \checkmark	(3 marks)
6	(C)	can di	stinguish signals \checkmark which have frequencies close to each other \checkmark	(2 marks)
6	(d)	(i)	frequency modulation ✓	(1 mark)
6	(d)	(ii)	amplitude modulation√	(1 mark)
6	(e)	I		







correct shape√

approx. in phase with audio \checkmark

(4 marks)

Total Mark: 18

Question 7



(5 marks)



7

(d)

The marking scheme for this part of the question includes an assessment of the Quality of Written Communication (QWC). There are no discrete marks for the assessment of written communication but QWC will be one of the criteria used to assign the answer to an appropriate level below.

		Descriptor		
Level	Marks	an answer will be expected to meet most of the criteria in the level descriptor		
3	4-5	 answer is full and detailed and is supported by an appropriate range of relevant points such as those given below argument is well structured with minimal repetition or irrelevant points accurate and clear expression of ideas with only minor errors in the use of technical terms, spelling, punctuation and grammar 		
2	2-3	 answer has some omissions but is generally supported by some of the relevant points below the argument shows some attempt at structure the ideas are expressed with reasonable clarity but with a few errors in the use of technical terms spelling, punctuation and grammar 		
1	0-1	 answer is largely incomplete, it may contain some valid points which are not clearly linked to an argument structure unstructured answer errors in the use of technical terms, spelling, punctuation and grammar or lack of fluency 		
		An example of the type of answer that may be produced would be: The traffic sensor signal is read and not activated leading to the short delay of 20s before the green light is switched off and the amber light is switched on. There is then a delay of 3s before the amber light is switched off and the red light is switched on. After a further 20s delay the amber light is switched on again for 3s before both amber and red lights are switched off when the green light is switched on again and cycle is repeated.		

(5 marks)

(i)

8 (a) A can cannot break the beam to S1 without also breaking beam to S2 and S3✓

(1 mark)

8 (a) (ii) 1 1 1 0 1 1 0 1 1 0 0 0 0 1 0 0 (4 marks) S1 S2 S3 8 S1 to AND gate ✓ (a) (iii) S2 to same AND gate√ S3 to NOT gate ✓ Both outputs to another AND gate \checkmark (4 marks) 8 (b) (i) 0 1 1 1 0 0

0

0

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(3 marks)





0

1

~

1

0

~





9



(3 marks)

9



Total Mark: 22

10 (a)

The marking scheme for this part of the question includes an assessment of the Quality of Written Communication (QWC). There are no discrete marks for the assessment of written communication but QWC will be one of the criteria used to assign the answer to an appropriate level below.

Level	Marks	Descriptor
Level	,	descriptor
3	4-5	 answer is full and detailed and is supported by an appropriate range of relevant points such as those given below argument is well structured with minimal repetition or irrelevant points accurate and clear expression of ideas with only minor errors in the use of technical terms, spelling, punctuation and grammar
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1	0-1	 answer is largely incomplete, it may contain some valid points which are not clearly linked to an argument structure unstructured answer errors in the use of technical terms, spelling, punctuation and grammar or lack of fluency
		 An example of the type of answer that may be produced would be: The sensor S₂ gives a high output which causes output Q from the flip-flop to go high. The electrically operated valve opens and water flows into the tank until the sensor S₁ is covered. (The output of sensor S₂ becomes low but this does not affect the output of the flip-flop.) The sensor S₁ gives a high output which resets the flip-flop, meaning that its output Q is low, so the electrically operated valve stops the flow of water.

(5 marks)



AQA



potential divider to non-inverting input \checkmark thermistor in correct position \checkmark

(2 marks)

10 (g) 1600Ω√√

(2 marks)

Total Mark: 23

Paper Total: 150