



GCSE

Electronics

44301

Mark scheme

4430

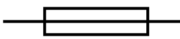
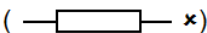




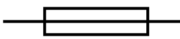
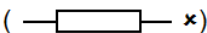




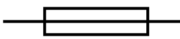
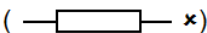




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Version: 1.0 Final

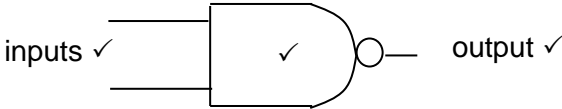
Mark schemes are prepared by the Lead Assessment Writer and considered, together with the relevant questions, by a panel of subject teachers. This mark scheme includes any amendments made at the standardisation events which all associates participate in and is the scheme which was used by them in this examination. The standardisation process ensures that the mark scheme covers the students' responses to questions and that every associate understands and applies it in the same correct way. As preparation for standardisation each associate analyses a number of students' scripts. Alternative answers not already covered by the mark scheme are discussed and legislated for. If, after the standardisation process, associates encounter unusual answers which have not been raised they are required to refer these to the Lead Assessment Writer.

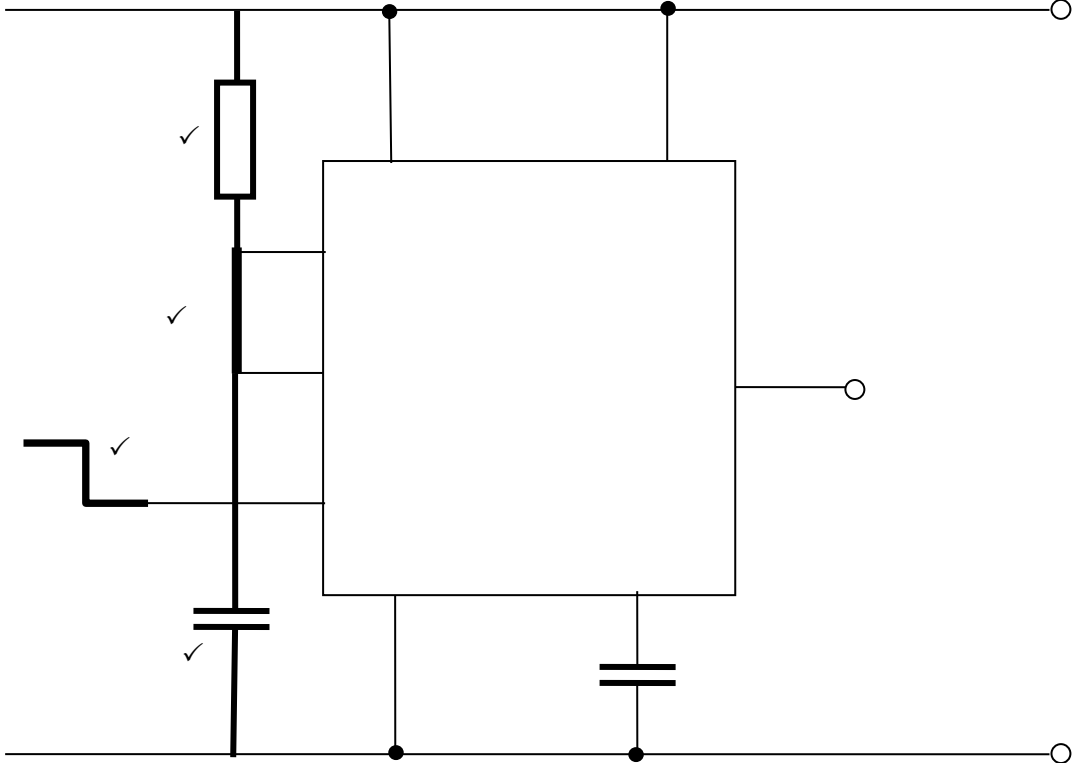
It must be stressed that a mark scheme is a working document, in many cases further developed and expanded on the basis of students' reactions to a particular paper. Assumptions about future mark schemes on the basis of one year's document should be avoided; whilst the guiding principles of assessment remain constant, details will change, depending on the content of a particular examination paper.

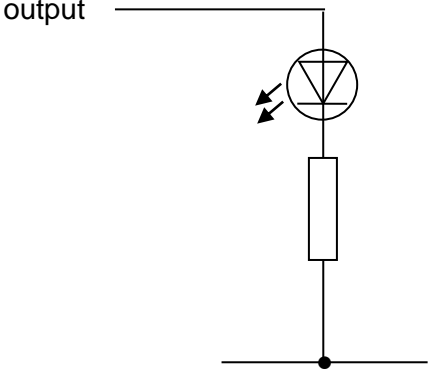
Further copies of this mark scheme are available from aqa.org.uk

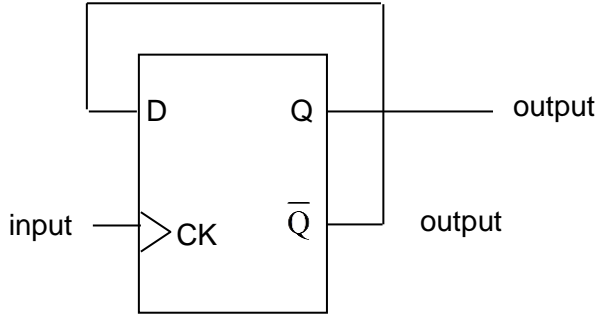
Question	Answers	Additional Comments/Guidance	Mark								
1a	(skin) burns / fibrillation = irregular heart rate / muscular spasm = stop heart / arrest breathing / damage internal organs ✓✓	(2 max)	2								
1b	call for help / turn off supply = remove from supply (using insulator) / place in recovery position / clear airways / CPR (if trained) / check pulse / check breathing ✓✓✓	(3 max)	3								
1c	<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 40%;">Name</th> <th>Symbol</th> </tr> </thead> <tbody> <tr> <td>Fuse ✓</td> <td> ✓ ( *)</td> </tr> <tr> <td>Electrolytic capacitor ✓</td> <td> ✓ ✓</td> </tr> <tr> <td>(step down) transformer ✓</td> <td>  ✓ Can be loops  Not  Any no. of lines between coils and can be dashed </td> </tr> </tbody> </table>	Name	Symbol	Fuse ✓	 ✓ ( *)	Electrolytic capacitor ✓	 ✓ ✓	(step down) transformer ✓	 ✓ Can be loops  Not  Any no. of lines between coils and can be dashed		7
Name	Symbol										
Fuse ✓	 ✓ ( *)										
Electrolytic capacitor ✓	 ✓ ✓										
(step down) transformer ✓	 ✓ Can be loops  Not  Any no. of lines between coils and can be dashed										
Total			12								

Question	Answers	Additional Comments/Guidance	Mark
2ai	timer✓ light sensor✓		2
2aii	lamp✓		1
2bi	logic✓		1
2bii	light sensor✓		1
2biii	driver✓		1
2biv	comparator✓		1
2ci	light sensor✓		1
2cii	logic✓ accept driver		1
2ciii	comparator✓		1
Total			10

Question	Answers	Additional Comments/Guidance	Mark															
3ai	<table border="1" data-bbox="322 432 1039 632"> <thead> <tr> <th>Sensor 1</th> <th>Sensor 2</th> <th>Output</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>1</td> </tr> <tr> <td>0</td> <td>1</td> <td>1</td> </tr> <tr> <td>1</td> <td>0</td> <td>1</td> </tr> <tr> <td>1</td> <td>1</td> <td>0</td> </tr> </tbody> </table> <p data-bbox="1070 520 1093 544">✓</p> <p data-bbox="1070 600 1093 624">✓</p>	Sensor 1	Sensor 2	Output	0	0	1	0	1	1	1	0	1	1	1	0		2
Sensor 1	Sensor 2	Output																
0	0	1																
0	1	1																
1	0	1																
1	1	0																
3aii	NAND ✓		1															
3aiii			3															

3bi	 <p>The diagram shows a circuit with a central square component. On the left, a vertical wire has four components connected to it: a resistor (marked with a checkmark), a component with a vertical line and a horizontal bar (marked with a checkmark), a component with a horizontal line and a vertical bar (marked with a checkmark), and a battery (marked with a checkmark). The top and bottom horizontal wires have two connection points each, marked with solid black dots. A second battery is connected to the bottom wire. On the right, the central square component has a single terminal connected to the bottom wire, and the top and bottom wires have open terminals.</p>		4
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3bii	 <p>output</p>	Symbol for LED ✓ Orientation of LED ✓ Resistor symbol in series ✓	3
Total			13

Question	Answers	Additional Comments/Guidance	Mark
4a	correct symbol ✓ with polarity ✓ in parallel with R ✓		3
4bi	$9 - 3.3 = 5.7 \text{ (V)}$ ✓		1
4bii	0.005 (A) ✓		1
4biii	$R = V/I$ ✓ = $5.7/0.005$ ✓ = $1140 \text{ (}\Omega\text{)}$ ✓ 2 max or ecf		2
4biv	$1200 \text{ (}\Omega\text{)}/1.2\text{k(}\Omega\text{)}$ ✓ or ecf		1
4ci	<p>D ✓ to bar Q ✓ CK as input ✓ Q and \bar{Q} as outputs ✓</p> 		4

4cii	<p>Frequency halved ✓ In phase with astable ✓</p> <p>Inverse of Q ✓</p>		3
Total			15

Question	Answers	Additional Comments/Guidance	Mark
5ai	convert electrical energy✓ into sound energy / produce sound✓		2
5aii	select/choose✓ the desired frequency/station✓ (from all the others)		2
5bi	amplitude✓	Accept voltage or power	1
5bii	A carrier wave at constant frequency ✓ varying in amplitude at a frequency significantly lower than the carrier frequency✓		2
5c	In an analogue signal the amplitude of the signal varies (with time) ✓ and can have a range of values✓ between a minimum and a maximum value.✓ In a digital signal the amplitude can only have one of two values✓ which are represented by 0 and 1. ✓ A simple radio receiver is an analogue system✓because the (radio and audio) signals have a range of values. ✓ (See notes on QWC below)		5
Total			12

QWC Mark Scheme

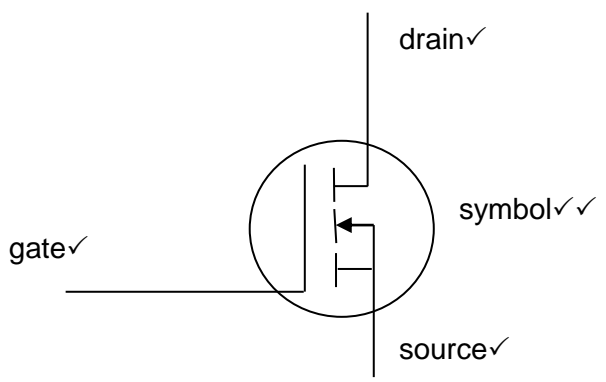
question	answers	extra information	mark
<p>Marks awarded for this answer will be determined by the Quality of Written Communication (QWC) as well as the standard of the scientific response. Examiners should apply a 'best-fit' approach to the marking.</p>			
<p>Level 1 (0—1 marks)</p>			
<p>Answer is largely incomplete. It may contain valid points which are not clearly linked to an argument structure. Unstructured answer . Errors in the use of technical terms, spelling, punctuation and grammar or lack of fluency.</p>			
<p>Level 2 (2—3 marks)</p>			
<p>Answer has some omissions but is generally supported by some of the relevant points below: - the argument shows some attempt at structure - the ideas are expressed with reasonable clarity but with a few errors in the use of technical terms, spelling, punctuation and grammar.</p>			
<p>Level 3 (4—5 marks)</p>			
<p>Answer is full and detailed and is supported by an appropriate range of relevant points such as those given below: - argument is well structured with minimum repetition or irrelevant points - accurate and clear expression of ideas with only minor errors in the use of technical terms, spelling and punctuation and grammar.</p>			
<p>examples of the points made in the response</p> <p>In an analogue signal the amplitude of the signal varies (with time) ✓ and can have a range of values ✓ between a minimum and a maximum value. ✓ In a digital signal the amplitude can only have one of two values ✓ which are represented by 0 and 1. ✓ A simple radio receiver is an analogue system ✓ because the (radio and audio) signals have a range of values. ✓ (5 maximum)</p>	<p>extra information</p> <p>Example only.</p>		

Question	Answers	Additional Comments/Guidance	Mark
6 a)			4
6 b)	<p>input = either “detect” box ✓ output = “display” or “release” box ✓ decision = diamond ✓ loop = arrow upwards ✓ process=any rectangle ✓</p>		5

6 c	Detect weight✓ Compare weight✓ Weight OK, accept coin and record value✓ Weight not OK reject✓ Join outcomes and end✓	Minus 1 mark for incorrect box shape	5
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Total			14
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6 c example	<pre> graph TD Start([start]) --> Detect[/detect weight of coin/] Detect --> Compare[compare weight with weight of a valid coin] Compare --> Decision{does weight match weight of a valid coin?} Decision -- Y --> Record[record value of coin] Record --> Accept[/accept coin into coinbox/] Accept --> End([end]) Decision -- N --> Reject[/reject coin/] Reject --> End </pre>		
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Question	Answers	Additional Comments/Guidance	Mark
7ai	non-inverting ✓		1
7aii	two resistors in series ratio 1:2 ✓ values 1k to 100k ✓		3
7aiii	in range 4 to 6 V inclusive ✓		1
7aiv	in range 0 to 2 V inclusive ✓		1
7bi	10 (°C) ✓		1
7bii	120 (Ω) ✓		1
7biii	240 Ω ✓✓	Answer by ratio/formula ✓	2
7c	 <p>drain ✓ gate ✓ source ✓ symbol ✓✓</p>	symbol ✓✓ gate to comparator ✓ source to 0V ✓ drain to buzzer ✓	5
7d	1 st and 2 nd (blue and grey) ✓ 3 rd brown ✓ 4 th gold ✓		3
Total			18

Question	Answers	Additional Comments/Guidance	Mark
8a	Capacitor 0 V to trigger ✓ resistor discharge to threshold and trigger ✓ polarity of C marked ✓ link ground to 0 V. ✓		4
<p>The diagram shows a central square component with four main terminals. The top-left terminal is connected to a vertical chain of three resistors. The top-right terminal is connected to a horizontal line that also passes through a resistor. The bottom-left terminal is connected to a battery symbol with a '+' sign and a checkmark. The bottom-right terminal is connected to a capacitor symbol with a checkmark. There are also two output terminals on the right side of the component, one at the top and one at the bottom, both connected to horizontal lines that extend to the right edge of the diagram.</p>			

8b	$T = (5 + 2 \times 200) \times 10^3 \times 2200 \times 10^{-6} / 1.44 \checkmark = 6.19 \checkmark \times 10^2 \text{ (s)} \checkmark$		3
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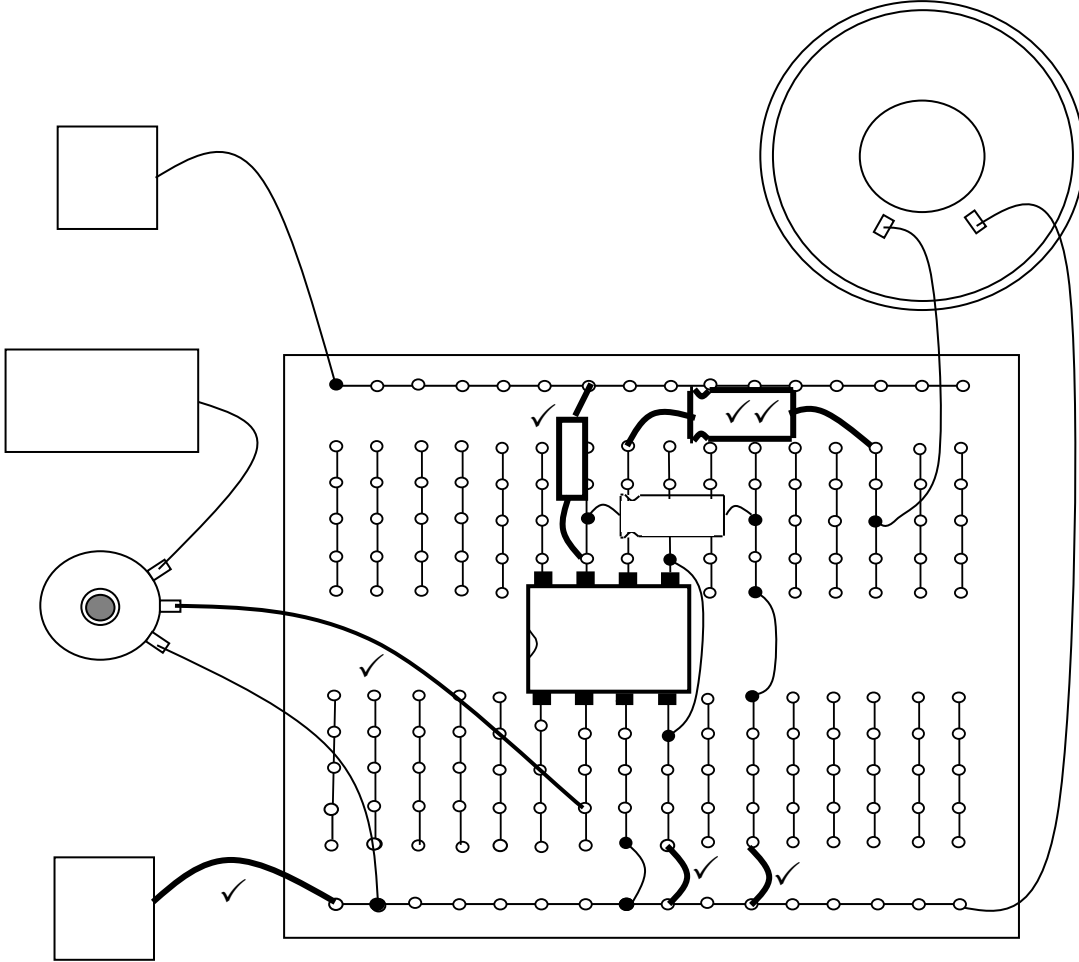
8ci	1000 \checkmark		1
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8cii	5 \checkmark		1
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8di	<p>outputs 6,7 and 8 ORed together \checkmark to heater 2 \checkmark to heater 1 \checkmark</p>		3
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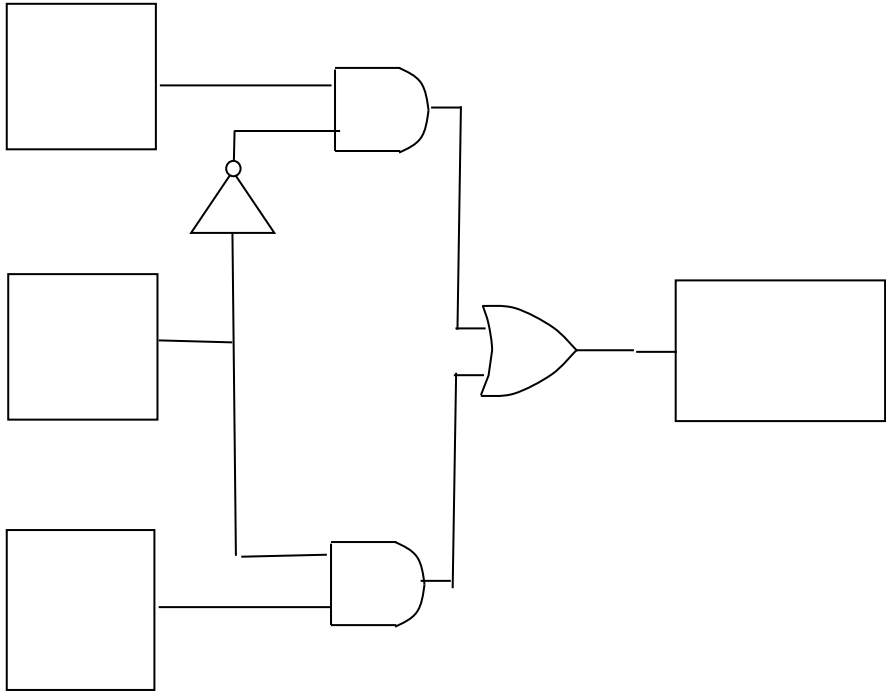
8dii	Connect to Q9 output (and zero volts)		1
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<p>8diii</p>	<p>✓ correct freq. and relationship to outputs</p>		<p>5</p>
<p>Total</p>			<p>18</p>

Question	Answers	Additional Comments/Guidance	Mark
9a		<p>Two wires in same hole minus 1</p> <p>1 mark for capacitor the right way</p>	7

9bi	$3 \div 3 = 1 \checkmark$ V/div \checkmark		2
9bii	Gain = $V_o/V_{in} = 3/0.2 \checkmark = 15 \checkmark$		2
9biii	($V_{rms} = V/\sqrt{2} = 3/\sqrt{2} \checkmark = 2.1V \checkmark$)		2
9biv	$P=VI = 2.12 \times 0.26 \checkmark = 0.56 W \checkmark \Rightarrow P=V^2/R = 2.12^2/R$		2
9bv	$2 \times 5 \checkmark = 10 ms \checkmark$		2
9bvi	$f = 1/T = 1/0.01 \checkmark = 100 Hz \checkmark$ (0.1 <u>Hz</u> = 1 mark)		2
9c	Range of frequencies \checkmark over which half rated(max) power is produced/over which output voltage is $V_{max}/\sqrt{2} \checkmark$		2
9d	act as heat sink/cooling/dissipate heat /remove excess heat by conduction to copper $\checkmark\checkmark$ to produce a very low resistance connection to 0 V to aid the stability of the amplifier \checkmark		2 max
Total			23

Question	Answers	Additional Comments/Guidance	Mark																								
10ai	NOR		1																								
10aii	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 25%;"></td> <td style="width: 25%;"></td> <td style="width: 25%;"></td> <td style="width: 25%;"></td> </tr> <tr> <td></td> <td></td> <td>1</td> <td></td> </tr> <tr> <td></td> <td></td> <td>0</td> <td></td> </tr> <tr> <td></td> <td></td> <td>0</td> <td></td> </tr> <tr> <td></td> <td></td> <td>0</td> <td></td> </tr> <tr> <td></td> <td></td> <td>✓</td> <td></td> </tr> </table>							1				0				0				0				✓			1
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10aiii	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 25%;"></td> <td style="width: 25%;"></td> <td style="width: 25%;"></td> <td style="width: 25%;"></td> </tr> <tr> <td></td> <td></td> <td>1</td> <td>0</td> </tr> <tr> <td></td> <td></td> <td>0</td> <td>1</td> </tr> <tr> <td></td> <td></td> <td>1</td> <td>0</td> </tr> <tr> <td></td> <td></td> <td>0</td> <td>0</td> </tr> <tr> <td></td> <td></td> <td>✓</td> <td>✓</td> </tr> </table>							1	0			0	1			1	0			0	0			✓	✓		3
		1	0																								
		0	1																								
		1	0																								
		0	0																								
		✓	✓																								
10aiv	Only one LED is lit at a time ✓ / Current/voltage same for either LED lit ✓		1 max																								
10av	LED 2 ✓ Neither ✓ LED 1 ✓		3																								

<p>10b</p>	<p>Sensor 1 to AND ✓, sensor 2 to another AND ✓, SW to top AND via NOT ✓, SW to lower AND ✓ Both ANDs to OR ✓ OR to boiler ✓</p> 		<p>6</p>
<p>Total</p>			<p>15</p>