GCSE 2004 June Series



# Mark Scheme

## Electronics 3432 (Higher Tier)

Mark schemes are prepared by the Principal Examiner and considered, together with the relevant questions, by a panel of subject teachers. This mark scheme includes any amendments made at the standardisation meeting attended by all examiners and is the scheme which was used by them in this examination. The standardisation meeting ensures that the mark scheme covers the candidates' responses to questions and that every examiner understands and applies it in the same correct way. As preparation for the standardisation meeting each examiner analyses a number of candidates' scripts: alternative answers not already covered by the mark scheme are discussed at the meeting and legislated for. If, after this meeting, examiners encounter unusual answers which have not been discussed at the meeting they are required to refer these to the Principal Examiner.

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### **Higher** Tier

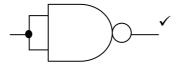
<b>1</b> (a)	diode√	
(b)	thermistor✓	
(c)	loudspeaker√	
(d)	relay✓	
(e)	three terminal regulator/zener diode	(5marks)
<b>2</b> (a)	demodulator√	
(b)	rf tuned circuit✓	
(c)	loudspeaker✓	
(d)	(audio)amplifier✓	

aerial/antenna√ (e)

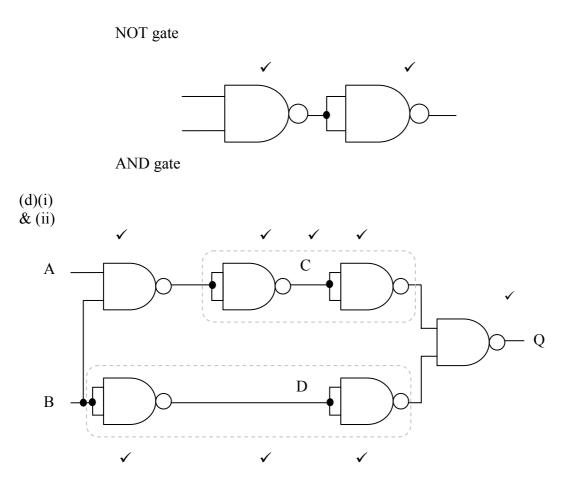
**3** (a)

А	В	<u> </u>	D	<u>Q</u>
0	0	0	1	11
0	1	0	0	0√
1	0	0	1	1√
1	1	1	0	11

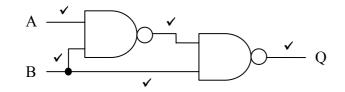
- (b) (i) LDR✓
  - (ii) microswitch✓
  - (iii) door open√ dark√
- NOR gate  $\checkmark$ (c) (i)



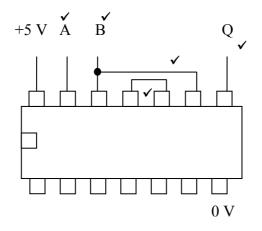
(5 marks)



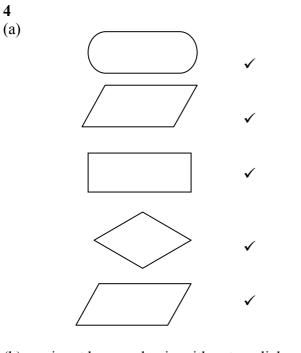
(iii)



(e)



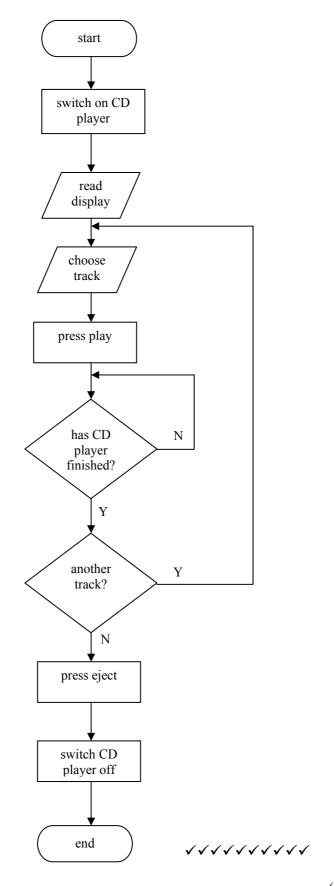
(30 marks)



(b) input boxes – begin with enter, click or input ✓ output box – print out ✓ compare box – diamond ✓ process box – rectangle ✓ loop – extreme right of flowchart, above or below end application ✓

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#### (c) (Example only)



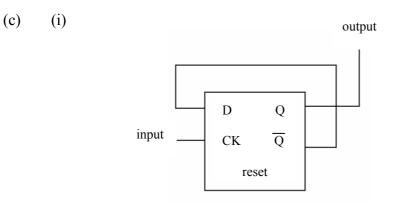


#### 5

(a) 
$$V_Y = 9 R_1 / (R_1 + R_2) \checkmark = 4.5 \checkmark V \checkmark$$

(b) (i) 3 kΩ✓

- (ii) 1.1 to 1.2 k $\Omega \checkmark$
- (iii)  $V_X > V_Y \checkmark$
- (iv)  $R_{th} = 2 k\Omega \checkmark, R_3 = 2 \checkmark k\Omega \checkmark (2 max)$
- (v) can vary  $\checkmark$  the light level at which switching occurs  $\checkmark$  if light level varies in the room  $\checkmark$  (2max)



input labelled  $\checkmark$  to clock  $\checkmark$ 

D to bar  $\overline{Q}\checkmark$ output labelled  $\checkmark$ 

- (ii) Q (or  $\overline{Q}$ ) of first  $\checkmark$  to CK input of second  $\checkmark$
- (d) (i) 0111√
  - (ii) 10**√**
  - (iii) 15/1111**√**

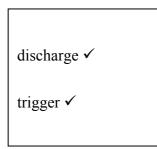
(19 marks)

**6** (a)

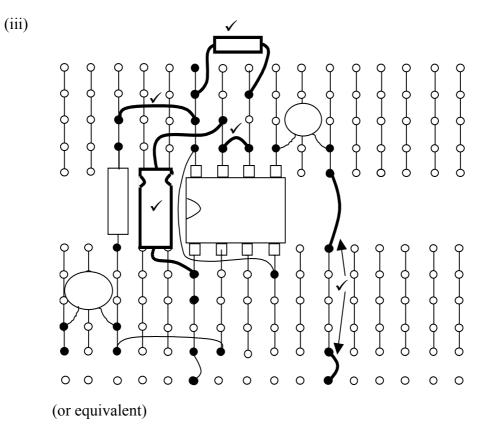
	1	1	1	1	•
	1	1	0	1	v
	1	1	1	1	,
	1	1	0	1	
	0	0	1	1	
	0	0	0	0	
	0	1	1	1	
	0	1	0	1	

Max 6 (6 minus one for any row incorrect)

(b) (i)



(ii) the voltage rises/goes high  $\checkmark$  for a definite/set time  $\checkmark$  which depends on the values of R and C  $\checkmark$  (2 max)



(15 marks)

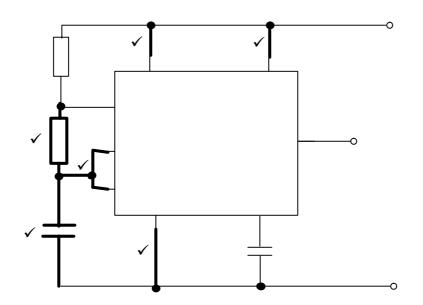
7							
(a)	(i)	8✓					
	(ii)	2,6✓					
	(iii)	14, 7✓					
(b)	(i)	$4 \times 2\checkmark = 8\checkmark \text{ ms}\checkmark$					
	(ii)	$f=1/T \checkmark = 125 \checkmark Hz \checkmark$					
	(iii)	$3 \times 0.1 \checkmark = 0.3 \checkmark (V)$					
	(iv)	$V_{out} = 20 \times 0.3 \checkmark = 6\checkmark (V)$					
		correct amplitude (3 squares)✓ et period (4 squares)✓ bidal ✓					
(c)	I = V/	I = V/R and P = VI $\checkmark$ , P=V <sup>2</sup> /R $\checkmark$ =1.13 $\checkmark$ W $\checkmark$ (3 max)					
(d)	amplitude/height of trace decreases ✓						

(d) amplitude/height of trace decreases ✓ period decreases/waveforms closer together ✓

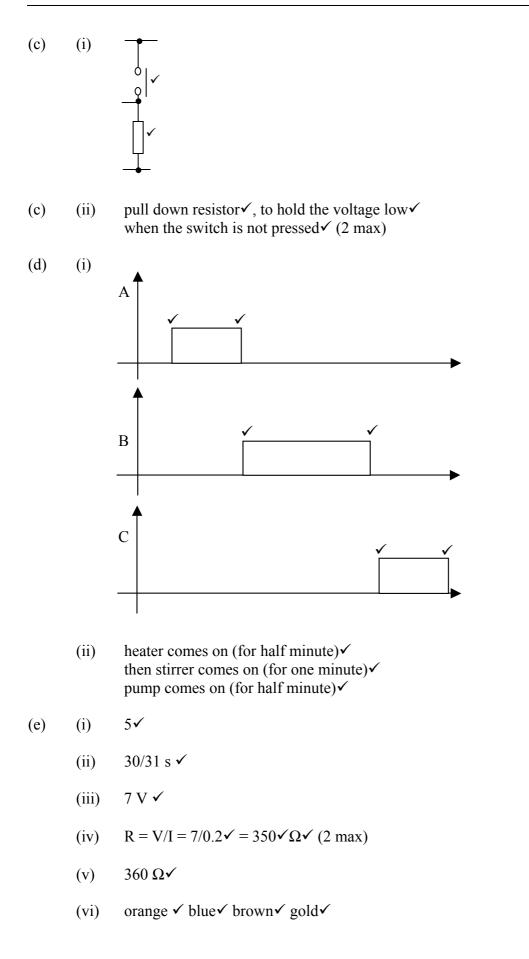
(21 marks)

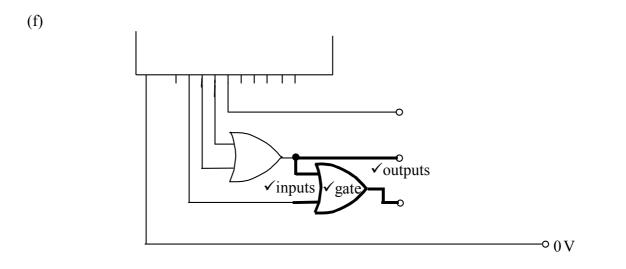


(a)



(b)  $T = (R_1 + 2R_2)C/1.44\checkmark = 31\checkmark s\checkmark$ 





(35 marks)

(Paper total 150 marks)