GCSE 2004 June Series



Mark Scheme

Design and Technology: Electronic Products (3551 – Short Course Foundation)

Mark schemes are prepared by the Principal Examiner and considered, together with the relevant questions, by a panel of subject teachers. This mark scheme includes any amendments made at the standardisation meeting attended by all examiners and is the scheme which was used by them in this examination. The standardisation meeting ensures that the mark scheme covers the candidates' responses to questions and that every examiner understands and applies it in the same correct way. As preparation for the standardisation meeting each examiner analyses a number of candidates' scripts: alternative answers not already covered by the mark scheme are discussed at the meeting and legislated for. If, after this meeting, examiners encounter unusual answers which have not been discussed at the meeting they are required to refer these to the Principal Examiner.

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understanding or skills relevant to the question will receive appropriate credit for their answers.
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The answers given in the following mark schemes are neither exhaustive nor exclusive. Candidates whose answers do not appear directly on the mark scheme, but who have demonstrated knowledge,

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GENERAL CERTIFICATE OF SECONDARY EDUCATION

Summer Examination 2004

Design and Technology: Electronic Products

Short Course: Foundation Tier

Question 1

(a)	(i) (ii) (iii) (iv)	Light Dependant Resistor, LDR. Variable Resistor, Potentiometer, Preset Resistor. Resistor Transistor.	(1 mark) (1 mark) (1 mark) (1 mark)
(b)	Input Proces Output	(2 marks) (2 marks) (1 mark)	
(c)	Rises.		
(d)	To detect light change, to control the switching of the transistor. To act as part of the potential divider, resistance changes when it gets dark and this acts with the VR to change the base voltage. Any other appropriate response – 1 mark each (2 marks)		
(a)			
(e)	Collector, base, emitter.		
(f)	(i)	Double and throw – 1 mark each	
		1 mark for simple response, 2 marks for qualified response	(2 marks)
	(ii)	e.g. The lamp will not drain the battery. RL once switched will require less current.	(2 marks)

Total 19 marks

(a)	Monostable.				(1 mark)
(b)	Feature e.g.	e 1 mark, LED IC	Orientation 1 mark - short leg, flat side - to 0V - dimple, dot, notch - dot indicates Pin 1	(1 mark) (1 mark) (1 mark) (1 mark)	(2 marks) (2 marks)
(c)	 (i) C1, capacitor, polorised or electronic capacitor (ii) To start the time delay, set the timer, start the timing period, trigger pin 2, or any valid reason 			(1 mark) (1 mark)	
(d)	Formula Substituting correct values Answer with units			(1 mark) (1 mark) (1 mark)	
(e)	Multimeter, avometer etc.			(1 mark)	
(f)	(i) (ii)	9 or 9V 0 or 0 o			(1 mark) (1 mark)

Total 13 marks

(a)	Detailed designs showing materials and suitable construction methods with appropriate location of switches and LED.	7 - 9 marks				
	Designs which show and suggest materials and construction methods for each case.	4 – 6 marks				
	Maximum 4 marks for single complete design					
	Basic designs which show materials and construction method for at least one case.	1-3 marks	(9 marks)			
	Quality of drawings:					
	Detailed and accurate drawings using appropriate techniques. Well drawn and clearly recognisable designs A basic drawing without detail or lacking any element of accuracy. Unrecognisable as a design for a container	(3 marks) (2 marks) (1 mark) (0 marks)	(3 marks)			
(b)	Specific material		(1 mark)			
	Identify construction method Explanation / suitability of form	(1 mark) (1 mark)	(2 marks)			
	 Some dimensions added Large enough to hold circuit, battery and components 	(1 mark) (1 mark)	(2 marks)			
	 Basic indication of access, e.g. battery panel Greater detail for both circuit and battery Full detail of access 	(1 mark) (2 marks) (3 marks)	(3 marks)			
	Each component appropriately located (1:	mark for each)	(3 marks)			
	Quality of drawing:					
	Detailed and accurate drawings using appropriate techniques. Well drawn and clearly recognisable design with some additional detail. A basic drawing lacking detail.	(3 marks) (2 marks) (1 mark)	(3 marks)			

Total 26 marks

(a)	Suitabl	(5 marks)	
(b)	(i)	Simple response – 1 mark e.g. not enough power Qualified response – 2 marks	(2 marks)
	(ii)	Simple response - 1 mark Qualified response - 2 marks	(2 marks)
			Total 9 marks
Ques	stion 5		
(a)	(i) (ii)	Printed circuit board. Computer Aided Design.	(1 mark) (1 mark)
(b)	(i)	Track. Pad	(1 mark) (1 mark)
	(ii)	Tracks thicker, end of tracks joined closer to pads/other tracks. No cross tracks, smaller circuit. Larger pads/ thicker line, add text, any valid response. Any three.	(3 marks)
(c)		statement 1 mark. Qualified statement or 2 single ents - 2 marks	
	e.g.	Activity undertaken during making vero, CAM or Photo etch Tools and equipment suitable for activity. Health and safety linked to activity. Quality Issues linked to activity.	(2 marks) (2 marks) (2 marks) (2 marks)
	Avoid not in	(2 marks)	
	Keep i	(2 marks)	

Total 19 marks

(a)	The material can be recycled.	(1 mark)

(b) (i) Plastic – disposal of waste, pollution during manufacture, non renewable, any other appropriate response.

(1 mark)

(ii) Metal - disposal of waste as recycled values fluctuate, cost of recycling, any other appropriate response.

(1 mark)

(c) To protect – prevent damage to product.

To inform – provide instructions as to use.

To market – attractive packaging to help promote.

Any two (2 marks)

(d) (i) Smaller sizes, lightweight, many functions, other suitable developments.

Any three (3 marks)

(ii) Consumer – used more, expensive bills, possible (1 mark)

health risk, more attractive to thieves.

Society – noise, intrusion, anti social, crime. (1 mark) Environment – disposal, batteries/cases, masts. (1 mark)

(e) Smaller, lighter, other functions - games, interactive games, video, camera live transmissions.

Any viable three. (3 marks)

Total 14 marks