GCSE 2004 June Series



Mark Scheme

Design and Technology: Electronic Products (3541 – Full Course Foundation)

Mark schemes are prepared by the Principal Examiner and considered, together with the relevant questions, by a panel of subject teachers. This mark scheme includes any amendments made at the standardisation meeting attended by all examiners and is the scheme which was used by them in this examination. The standardisation meeting ensures that the mark scheme covers the candidates' responses to questions and that every examiner understands and applies it in the same correct way. As preparation for the standardisation meeting each examiner analyses a number of candidates' scripts: alternative answers not already covered by the mark scheme are discussed at the meeting and legislated for. If, after this meeting, examiners encounter unusual answers which have not been discussed at the meeting they are required to refer these to the Principal Examiner.

It must be stressed that a mark scheme is a working document, in many cases further developed and expanded on the basis of candidates' reactions to a particular paper. Assumptions about future mark schemes on the basis of one year's document should be avoided; whilst the guiding principles of assessment remain constant, details will change, depending on the content of a particular examination paper.

understanding or skills relevant to the question will receive appropriate credit for their answers.
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The answers given in the following mark schemes are neither exhaustive nor exclusive. Candidates whose answers do not appear directly on the mark scheme, but who have demonstrated knowledge,

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ASSESSMENT AND QUALIFICATIONS ALLIANCE

GENERAL CERTIFICATE OF SECONDARY EDUCATION

Summer Examination 2004

Design and Technology: Electronic Products

Full Course: Foundation Tier

Question 1

(a)	(i)	Light Dependant Resistor/LDR		(1 mark)
	(ii)	Variable Resistor or Potentiometer, Pre	set Resistor	(1 mark)
	(iii)	Resistor		(1 mark)
	(iv)	Transistor.		(1 mark)
(b)		- VR and LDR or A and B. (relate to (see Resistor and transistor or C and D. (ret t - Lamp/Bulb)		(2 marks) (2 marks) (1 mark)
(c)	Rises.			(1 mark)
(d)	To detect light change, to control the switching of the transistor. To act as part of the potential divider, resistance changes when it gets dark and this acts with the VR to change the base voltage or any other appropriate response. (1 mark each)			(2 marks)
(e)	Collector, base, emitter.			(3 marks)
(f)	(i)	Double throw.	(1 mark each)	(2 marks)
	(ii) 1 mark for simple responses, 2 marks for qualified reason. e.g. The lamp will not drain the battery / as RL once switched will require less current.			(2 marks)

Total 19 marks

(a)	Monostable.				(1 mark)
(b)	e.g.	LED	Feature 1 mark, Orientation 1 mark Short leg or flat side to 0V	(1 mark) (1 mark)	(2 marks)
	e.g.	IC	Dimple or dot Indicates Pin 1	(1 mark) (1 mark)	(2 marks)
(c)	C1 / Capacitor / Polarised or Electrolytic Capacitor				(1 mark)
(d)	T = R Subst Answ	(1 mark) (1 mark) (1 mark)			
(e)	Multimeter, avometer etc.			(1 mark)	
(f)	(i) (ii)	9V or 0 ohm			(1 mark) (1 mark)

Total 12 marks

(a)	Detailed designs showing materials and suitable construction methods with appropriate location of switches and LED.	7 – 9 marks						
	Designs which show and suggest materials and construction methods for each case.	4 – 6 marks						
	Maximum 4 marks for single complete design							
	Basic designs which show materials and construction method for at least one case.	1 – 3 marks	(9 marks)					
	Quality of drawings:							
	Detailed and accurate drawings using appropriate	(3 marks)						
	techniques. Well drawn and clearly recognisable designs A basic drawing without detail or lacking any	(2 marks)						
	element of accuracy. Unrecognisable as a design for a container	(1 mark) (0 marks)	(3 marks)					
(b)	Specific material		(1 mark)					
	• Identifying the construction method Explanation / suitability of form	(1 mark) (1 mark)	(2 marks)					
	Some dimensions added Large enough to hold circuit, battery and	(1 mark)	(2 marks)					
	components	(1 mark)	(2 marks)					
	 Basic indication of access, e.g. battery panel Greater detail for both circuit and battery Full detail of access 	(1 mark) (2 marks) (3 marks)	(3 marks)					
	Each component appropriately located	1 mark each	(3 marks)					
	Quality of drawing:							
	Detailed and accurate drawings using appropriate techniques. Well drawn and clearly recognisable design with some additional detail.	(3 marks) (2 marks)						
	A basic drawing lacking detail.	(1 mark)	(3 marks)					

(a) Suitable commands for light and time delay 1 mark each

e.g. Switch on 8

Switch on 4

Wait 0.1 or .1

Switch on 3 or similar reference to 1 and 2

Wait 0.2 or .2

(5 marks)

(b) (i) Simple response – 1 mark e.g. not enough power

Qualified response – 2 marks

e.g. The output from the PIC is only 100 mA and the lamps

require 500mA, the lamps will not light.

(2 marks)

(ii) Simple response - 1 mark

Qualified` response – 2 marks

e.g. The transistors will amplify the output current.

Current at a suitable level.

(2 marks)

Total 9 marks

(a)	(i) (ii) (iii)	(ii) Insulator			(1 mark) (1 mark) (1 mark) (1 mark)
(b)	Integrated. Circuit.				(1 mark) (1 mark)
(c)	(i)	14			(1 mark)
	(ii)	•	Pin 16 to +V Pin 8 to 0V	(1 mark) (1 mark)	(2 marks)
		•	Suitable switch Connected to Pin 15 and +V	(1 mark) (1 mark)	(2 marks)
		•	It is regretted that there was an error on the third bullet point on Q5(c)(ii) on the paper. This refers to the connections to the "2nd and 3rd LEDs", which should have read "3rd and 4th LEDs". As a result many possible answers were available, consequently the following marking strategy is to be adopted.		
			3 rd life LED connected to pin 4 (or 4 th life LED to pin 7)	(4 marks)	
			3 rd life LED connected to pin 7 (or 4 th life LED to pin 10)	(3 marks)	
			3 rd life LED or 4 th life LED connected to a output pin	nny (2 marks)	(4 marks)
	Any changes to the 2 nd life LED connection, which is shown correctly in the question, do not affect the marks available, as				

Any changes to the 2nd life LED connection, which is shown correctly in the question, do not affect the marks available, as they are determined primarily by the 3rd life LED connection.

non-digital

Quality of drawing:

Clear lines

	Straight vertical / horizontal Correct switch symbol	(1 mark) (1 mark)	(3 marks)
(iii)	Simple response e.g. switch bounce Qualified response e.g. multiple input	(1 mark) (2 marks)	

need clear input to chip

(1 mark)

(2 marks)

Total 20 marks

(a)	(i) (ii)	Printed circuit board. Computer Aided Design.	(1 mark) (1 mark)
(b)	(i) (ii)	Track. Pad (1 mark each) Tracks thicker, end of tracks joined closer to pads/other tracks; No cross tracks, smaller circuit, some pads/tracks too close; strain holds Larger pads/ thicker line; Add text to identify components or other valid response	(2 marks)
		(1 mark each)	(3 marks)
(c)	Single	statement – 1 mark	
	Qualified statement or 2 single statements – 2 marks		(2 marks)
	e.g.	Activity undertaken during making vero, CAM or Photo etch Tools and equipment suitable for activity. Health and safety linked to activity. Quality Issues linked to activity.	(2 marks) (2 marks) (2 marks) (2 marks)
	e.g.	Avoid heated parts, soldering iron/stand. Replace iron to stand when not in use. Work in ventilated area. Wash hands after use. Reasons appropriate to process.	
	e.g.	Keep iron tip clean. Warm both components and track. Avoid excessive heat. Avoid excessive use of solder.	(2 marks)

Total 19 marks

(a) The material can be recycled. (1 mark)

(b) (i) Plastic – disposal of waste, pollution during manufacture, non renewable energy used during manufacture, or any other appropriate response

> Any one (1 mark)

(ii) Metal - disposal of waste as recycled values fluctuate, cost of recycling energy used during manufacture, or any other appropriate response

> Any one (1 mark)

(c) To protect – prevent damage to product.

To inform – provide instructions as to use.

To market – attractive packaging to help promote.

(2 marks) Any two

(d) (i) Smaller sizes, lighterweight, many functions, other suitable developments.

> Any three (3 marks)

(ii) Consumer more attractive to thieves, used more, expensive bills, possible health

risk.

(2 marks) 2 - noise, intrusion, anti social, crime. (2 marks) Society 3 Environment disposal, batteries/cases, masts. (2 marks)

One word $(1 \times 3 \text{ marks})$ Some clarification (2 x 3 marks)

Developments e.g. Smaller, lighter, other functions, games, interactive games, video, camera live transmissions.

(3 marks)

Explanations. (3 marks)

Total 20 marks