



**General Certificate of Secondary Education
June 2012**

**Design and Technology: 45401
Electronic Products**

(Specification 4540)

Unit 1: Written Paper

Post-Standardisation

Mark Scheme

Mark schemes are prepared by the Principal Examiner and considered, together with the relevant questions, by a panel of subject teachers. This mark scheme includes any amendments made at the standardisation events which all examiners participate in and is the scheme which was used by them in this examination. The standardisation process ensures that the mark scheme covers the students' responses to questions and that every examiner understands and applies it in the same correct way. As preparation for standardisation each examiner analyses a number of students' scripts: alternative answers not already covered by the mark scheme are discussed and legislated for. If, after the standardisation process, examiners encounter unusual answers which have not been raised they are required to refer these to the Principal Examiner.

It must be stressed that a mark scheme is a working document, in many cases further developed and expanded on the basis of students' reactions to a particular paper. Assumptions about future mark schemes on the basis of one year's document should be avoided; whilst the guiding principles of assessment remain constant, details will change, depending on the content of a particular examination paper.

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COMPONENT NUMBER: 45401

COMPONENT NAME:

New Specification - GCSE Design and Technology (Electronic Products)

FOR EXAMINERS – PLEASE NOTE THAT IF YOU ARE UNSURE HOW TO AWARD A RESPONSE FROM A CANDIDATE, PLEASE SEEK CLARIFICATION OR ADVICE FROM YOUR TEAM LEADER OR THE PRINCIPAL EXAMINER.

Section A

Question	Part	Sub Part	Marking Guidance	Marks
1	a		<p>Alternative 1-1 mark for each correct or suitable response</p> <p>Accept all responses <u>not</u> duplicated Where several responses are tabled in a box accept first response</p> <p>Input : LDR, Switch, PTM, PTB, Micro switch, Photo transistor, photodiode, Reflective Opto-switch,</p> <p>Process: PIC, Microcontroller, 4017, 4026, Op Amp summing amplifier, Decade Counter</p> <p>Output: LEDs, LCD, Seven Segment Display, Lamps, Bulbs, Buzzer, Loudspeaker</p> <p>Alternative 2 -1 mark for each correct or suitable response</p> <p>Input: LDR, Switch, PTM, PTB, Micro switch, Photo transistor, photodiode, Reflective opto switch</p> <p>Process: PIC, Microcontroller, 4017, 4026, Op Amp summing amplifier</p> <p>Output: LEDs, LCD, Seven Segment Display, Lamps, Bulbs, Buzzer, Loudspeaker</p>	<p>1 mark</p> <p>1 mark</p> <p>1 mark</p> <p>1 mark</p> <p>1 mark</p> <p>1 mark</p> <p>Total 6 marks</p>
1	b		<p>1 mark each for the following features:</p> <p>Visual appealing product</p> <p>A space or location indicated for battery</p> <p>A clearly shown on /off switch location</p> <p>A clamp/bracket or fixture to attach to a table or board</p> <p>A clearly indicated gap or space through which balls roll</p> <p>An organised diagram or sketch with notes/annotation</p>	<p>1 mark</p> <p>1 mark</p> <p>1 mark</p> <p>1 mark</p> <p>1 mark</p> <p>1 mark</p> <p>Total 6 marks</p>

1	c	<p>Candidates might show:</p> <p>A system diagram, block diagram or circuit schematic</p> <p>A. The system can count functionally and will feature any of the following:</p> <ul style="list-style-type: none"> • a 4026 counter • a 4017 counter • a microcontroller <p>For a second mark they will detail or expand on the above with a drawing or notes:</p> <ul style="list-style-type: none"> • a pin out diagram • comments about how it counts • mention of de-bounce • mention of suitable input device <p>B. The system can be set/reset using a featured PTM or similar switch OR A simple reference to reset switch</p> <p>For a second mark the reset pin on the chip or block diagram is connected to a reset switch. OR is described as 'earthing' the pin to 0 volts or is shown connected via a pull up or pull down resistor</p> <p>C. Output pin(s) are shown to an audio output and visual output eg: speaker, buzzer, piezo sounder, 7 segment, LEDs, lamps</p> <p>They may also be described rather than drawn</p> <p>1 mark for each output shown</p>	<p>1 marks</p> <p>1 mark</p> <p>1 mark</p> <p>1 mark</p> <p>1-2 marks</p> <p>Total 6 marks</p>

1	d	<p>Constructional details</p> <p>Each of the following constructional details featured as images and or words:</p> <ul style="list-style-type: none"> • Material eg: Polystyrene, PVC, Acrylic, 1 mark • Process eg: Vacuum forming, injection moulding, fabrication 1 mark • Reference to size and dimensions in mm 1 mark • Location and retention of components eg: LED bezel, PCB pillars, battery clip, screws and washers 1 mark • Access to battery eg: compartment or lid 1 mark • Fixing to table top game eg: screw, clamp, sucker 1 mark <p>Circuit operation details</p> <p>In each case components drawn and identified</p> <ul style="list-style-type: none"> • Input drawn and described – basic 1 mark detailed and justified 2 marks 1-2 marks eg: sensor and detection • Process drawn and described – basic 1 mark detailed and justified 2 marks 1-2 marks • Output drawn and described – basic 1 mark detailed and justified 2 marks 1-2 marks <p>Communication of the design</p> <p>any one of the following:</p> <ul style="list-style-type: none"> • Basic drawing method used with no annotation 1 mark • 2 and/or 3D drawing method clear but with very little or no annotation or limited notes 2 marks • Detailed drawing / section view or breakout view with annotation 3 marks <p>Total 15 marks</p>	

Section B

Question	Part	Sub Part	Marking Guidance	Marks
2	a	i	Drain	1 mark
		ii	Gate	1 mark
		iii	Source	1 mark
				Total 3 marks
2	b		Simple response eg: low output for device / voltage or current / protect the IC	1 mark
			OR Qualified response: <ul style="list-style-type: none"> as an interface to protect the IC increase output current or voltage match power required in circuit 	2 marks
				Total 2 marks
2	c	i	Diode symbol correct	1 mark
			OR Correct diode symbol and polarity inserted between TR1 and Vcc 9 supply rail	2 marks
				Total 2 marks
2	c	ii	Simple response: <ul style="list-style-type: none"> to separate two circuits/ interface two circuits 	1 mark
			OR Detailed response: – <ul style="list-style-type: none"> to separate circuits at different voltages to increase operating voltage between circuits allow a small voltage to control a large voltage 	2 marks
				Total 2 marks
2	c	iii	Part response – any of the following: <ul style="list-style-type: none"> to switch higher loads to increase gain amplify input Darlington pair/ emitter follower pair power switching Increase current to match or drive the load Increased sensitivity 	1 mark
			Complete response – Any two of the above	2 marks
				Total 2 marks

3	a	i	<p>Bread boarding: Any response from list below Any two comments made or one explained in detail</p> <p>Advantages –</p> <ul style="list-style-type: none"> • Components can be swapped/changed • Real world sizes and values used • Circuit currents/voltages can be tested Easily • No need to solder or make PCB • Spread over a larger area than a PCB for • ease of test and probing <p>Or other suitable response</p>	<p>1 mark 2 marks</p> <p>Total 2 marks</p>
3	a	ii	<p>CAD: Any response from list below Any two comments made</p> <p>Advantages -</p> <ul style="list-style-type: none"> • Easy to model quickly or substitute • Real world PCB sizes established • Components can be tested without damage • Unavailable components can be used • Reduced development time • Prompts faults • Wide choice of alternatives can be explored • Speed of prototyping <p>Or other suitable response</p>	<p>1 mark 2 marks</p> <p>Total 2 marks</p>
3	b		<p>Simple response from any of below :</p> <ul style="list-style-type: none"> • Reduced production time • Lower costs (see last point) • Smaller sized assemblies/ smaller components • Improved reliability • Lower resistance values • Automation • Reasoned response as to why it costs less <p>Detailed response covering any two points above or a fully justified single response</p>	<p>1 mark</p> <p>2 marks</p> <p>Total 2 marks</p>

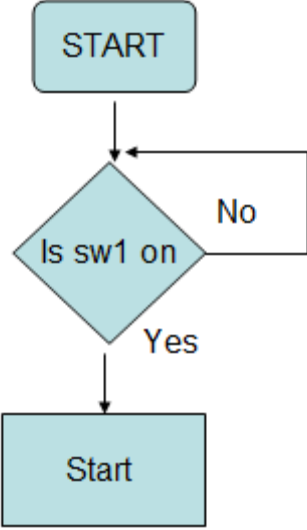
3	c		<p>Placement :</p> <ul style="list-style-type: none"> • Using a pick and place machine • Bandolier and insertion arm • Tweezers <p>Any above response:</p> <p>Attachment:</p> <ul style="list-style-type: none"> • Wave soldering • Reflow soldering (using solder paste) • Reflow gun OR oven • Also accept – soldered in place <p>Any above response:</p>	<p>1 mark</p> <p>1 mark</p> <p>Total 2 marks</p>
3	d		<p>Reasons :</p> <ul style="list-style-type: none"> • Components are small and difficult to handle • Components cannot be replaced easily • Components do not fit breadboard • Wide range of components not available • No oven/ reflow facility available <p>Any single reason from above 1 mark Any two reasons detailed from above 2 marks</p>	<p>1 mark</p> <p>1 mark</p> <p>Total 2 marks</p>

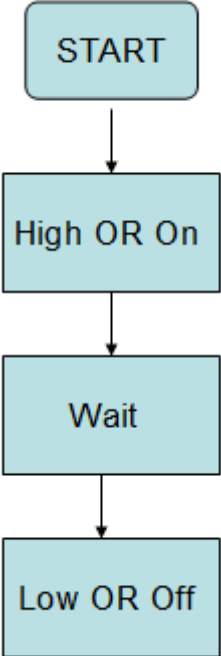
3	e	<p>NB: ignore the stated method</p> <p>Table for photo etched PCB Award marks for any correct responses only in sequence</p> <p>1 PCB layout from CAD or hand drawn onto Acetate 2 Acetate and sensitised copper (FR4) exposed to UV 3 PCB developed in Sodium Hydroxide 4 PCB etched in Ferric Chloride (FE3 CL) 5 Rinse stage 6 Drill</p> <ul style="list-style-type: none"> • Additional or alternative stages • Check for etching faults <p>Table for machined PCB</p> <p>1 PCB layout from CAD 2 CAD converted to CNC machine code 3 Clamp material to bed 4 Dry Copper coated board mounted on CNC table 5 Engrave using a 'Vee' cutter used to machine tracks 6 Drill to 'pop' and/or drill component holes</p> <p>Any of above in sequence for 1 mark each</p>	<p>1 mark 1 mark 1 mark 1 mark 1 mark 1 mark 1 mark</p> <p>OR</p> <p>1 mark 1 mark 1 mark 1 mark 1 mark</p> <p>Total 6 marks</p>
4	a	<p>Simple response: 1 mark</p> <p>A material that can change its state / or behaviour</p> <p>Detailed response: 2 marks</p> <p>Material that changes its properties or behaviour in response to external influence/stimulus</p> <p>Giving an example eg: smart wire, nitinol, thermochromic film</p>	<p>1-2 marks</p> <p>Total 2 marks</p>

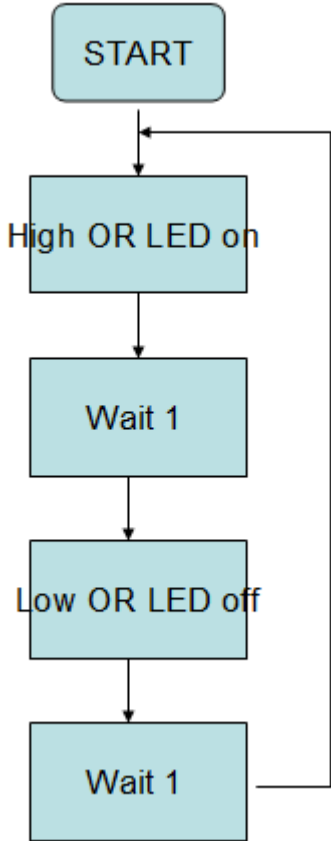
4	b	<p>Mark this answer across the two boxes as ‘application’ and ‘properties’ are often misplaced or misunderstood by candidates</p> <p>QTC Any response similar to below</p> <p>Application:</p> <ul style="list-style-type: none"> • Use as a pressure switch • Use as a variable resistor • Use as a gas sensor • In cable or sheet form as a vibration sensor <p>Property:</p> <ul style="list-style-type: none"> • An insulator(non-conductor) that becomes a conductor • When pressed it behaves as a variable resistor <p>EL Any response similar to below</p> <p>Application:</p> <ul style="list-style-type: none"> • As a backlight in products • Low intensity illumination • As a controlled decorative surface <p>Property:</p> <ul style="list-style-type: none"> • Electroluminescence • Thin film EL zinc coated • Glows when a high voltage (50+ volts) is applied <p>PV (Photovoltaic Cells) Any response similar to below</p> <p>Application:</p> <ul style="list-style-type: none"> • Used to charge batteries or capacitors in sunlight • Garden lighting • Low cost charging units for camping • Powering devices in remote locations eg: Desert, roadside, oceanic, space <p>Property:</p> <ul style="list-style-type: none"> • Generating electric power from solar energy • Solar sensitive P-N diode junction (sensitive to light) • Solar radiation induces a voltage between poles 	<p>1 mark</p> <p>1 mark</p> <p>1 mark</p> <p>1 mark</p> <p>1 mark</p> <p>1 mark</p> <p>Total 6 marks</p>
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4	c	i	<p>Simple response (one of the following only)</p> <ul style="list-style-type: none"> • products designed to fail • products with limited lifespan • Value engineered products (that will fail materially) • Products designed to fail after a period of time • Designed and sold to be outdated by new products • Style/fashion or built-in obsolescence <p>Detailed response of one point or commentary detailing two points</p>	<p>1 -2 marks</p> <p>Total 2 marks</p>
4	c	ii	<p>Any simple response from below</p> <ul style="list-style-type: none"> • Products that can be recycled • Use/own the product for a longer than planned period • Repair rather than dispose of a faulty product • Products that continue to be used • Products that are refused • Any 6R's relevant comment • Upgrade aspects of the product eg: software/apps <p>Any detailed response covering two of the above</p>	<p>1 mark</p> <p>OR</p> <p>2 marks</p> <p>Total 2 marks</p>
5	a	i	Astable	1 mark
	a	ii	An electronic device with two stable states	2 marks
	a	iii	Monostable	1 mark
				Total 4 marks
5	b	i	<p>1 mark each for the following:</p> <ul style="list-style-type: none"> • Correct LED symbol(s) • Correctly sinking to pin 3 • Correctly sourcing from pin 3 • 1-2 limiting resistors correctly positioned • Both LEDs will alternately flash (output will work) <p>Note : No R value required</p>	<p>1 mark</p> <p>1 mark</p> <p>1 mark</p> <p>1 mark</p> <p>1 mark</p> <p>Total 5 marks</p>
5	b	ii	<p>Diagram showing</p> <ul style="list-style-type: none"> • A square wave or similar • Space arrowed • Mark arrowed • Period (Mark + Space) arrowed 	<p>1 mark</p> <p>1 mark</p> <p>1 mark</p> <p>1 mark</p> <p>Total 4 marks</p>

5	b	iii	<p>Formulae used</p> <ul style="list-style-type: none"> • $0.693 \times R2 \times C$ OR $(.693RC)$ <p>From table at front of paper</p> <p>Simple substitution Calculation = $0.693 \times 100K \times 10\mu F$ 1mark</p> <p>OR</p> <p>Full transposition of values Calculation = $100000 \Omega \times .00001 F$ 2 marks</p> <p>Value Time Low</p> <ul style="list-style-type: none"> • 0.7 seconds rounded up OR 0.693seconds 	<p>1 mark</p> <p>1-2 marks</p> <p>1 mark</p> <p>Total 4 marks</p>
6	a		<p>Advantages</p> <p>1 mark for each advantage stated of:</p> <ul style="list-style-type: none"> • Can be reprogrammed to different frequencies • Less components required • More outputs available • More inputs available • Different /complex output sequences possible • Display can be updated easily • Smaller circuit possible • Lower power consumption possible • Microcontroller can also be changed 	<p>1 mark</p> <p>1mark</p> <p>1 mark</p> <p>Total 3 marks</p>
6	b		<p>Simple response from list - 1 mark</p> <p>Detailed response covering 2 points or justified single response - 2 marks</p> <ul style="list-style-type: none"> • Prevents damage from the soldering iron • Ease of insertion and removal of IC • Allows for different IC's to be used or tested • Easier to test probe a socket 	<p>1-2 marks</p> <p>Total 2 marks</p>

6	c	i	<p>Typical voltage for powering microcontrollers</p> <p>3-6 volts</p> <p>Note: Allow 3-6 volts for Some PIC and MSP devices are reported to operate at around 1 volt (these are <u>not typical</u> or in common use and rated use is 5 volts for most but students may have been taught and used other voltages).</p>	1 mark
6	c	ii	<p>Either of the following answers for 1 mark</p> <ul style="list-style-type: none"> • Voltage regulator • 7805 	1 mark
Total				1 mark
6	d	i	<p>Using lines, blocks or system shapes with labels the Chart may look similar to the following:</p> <div style="text-align: center;">  <pre> graph TD START([START]) --> SW1{Is sw1 on} SW1 -- No --> SW1 SW1 -- Yes --> Start[Start] </pre> </div> <p>Any arrows used may help diagram understanding 1 mark for decision 1 mark for start 1 mark for loop back</p>	1-3 marks
Total				3 marks

6	d	ii	<p>Using lines, blocks or system shapes with labels</p> <p>Any of the following in a viable sequence 1 mark for each or similar answer. Any shape of box acceptable.</p>  <pre>graph TD; START([START]) --> High[High OR On]; High --> Wait[Wait]; Wait --> Low[Low OR Off];</pre>	1-3 marks
				Total 3 marks

6	d	iii	<p>Using lines, blocks or system shapes with labels Any of the following in a viable sequence</p> <p>Or any similar or suitable response</p> <p>1 mark each for a high or on / wait / low or off / return loop NB: No mark available for 2nd wait</p>  <pre> graph TD START([START]) --> LED1[High OR LED on] LED1 --> WAIT1[Wait 1] WAIT1 --> LED2[Low OR LED off] LED2 --> WAIT2[Wait 1] WAIT2 --> LED1 </pre>	<p>1-3 marks</p> <p>Total 4 marks</p>

7	a	<p>PCB testing QWC question mark in one of the following bands:</p> <p>A high level response with a full and comprehensive explanation of all aspects of a suitable process. Response well structured with good use of appropriate design and technology terminology and showing a good grasp of grammar, punctuation and spelling. - (7– 8 marks)</p> <p>A medium level response with a good explanation of a Suitable process , however with some aspects of the process omitted. Response fairly well structured with some use of design and technology terminology with a small number of errors in grammar, punctuation. (5-6 marks)</p> <p>A low level response with a limited explanation of one part of the process with several errors. Response poorly structured with little or no use of design and technology terminology and with several errors in grammar, punctuation and spelling. (3-4 marks)</p> <p>An attempt at a response, no relevant description presented. No use of design and technology terminology and multiple errors in grammar, punctuation and spelling. (1-2 marks)</p> <p>Technical coverage will include some reference to several of these key words: Testing, bench-testing, destruction testing, continuity, resistance, multi-meter, magnifying glass, lamp test, probe, visual inspection, compare against original design</p> <p>Or other suitable or similar key words</p>	<p>1-8 marks</p> <p>Total 8 marks</p>

7	b	<p>Image 1</p> <p>Any of the following for 1 mark</p> <p>Name:</p> <ul style="list-style-type: none"> • Digital Probe • Signal Injector • Pulse injector • Test probe / logic probe (accept probe) <p>Purpose:</p> <p>Any of the following for 1 mark</p> <ul style="list-style-type: none"> • To test for voltage • To test for continuity • To test for or measure small signals eg: square wave, transients or logic levels <p>Or similar/ suitable response</p> <p>Image 2</p> <p>Name:</p> <p>Multi-meter (accept voltmeter)</p> <p>DVM</p> <p>Or similar/ suitable response</p> <p>Purpose:</p> <p>To check resistance/ voltage/current</p> <p>To check continuity</p> <p>To test voltages/levels</p> <p>To test components</p> <p>To check polarity</p>	<p>1 mark</p> <p>1 mark</p> <p>1 mark</p> <p>Total 4 marks</p>
7	c	<p>Quality Control :</p> <p>Any simple response for 1 mark and combined responses and justification from list below 2 marks</p> <ul style="list-style-type: none"> • Known as QC • Is about testing and measuring • Using 'methods' to check • Checking against a standard or specification • Is applied to products during manufacturing stages • Is to uncover defects or faults • Used to improve or optimise manufacture • Prevents sale/release of faulty parts/goods • To check batches • To help identify repairs and replacements for faulty production 	<p>1-2 marks</p> <p>Total 2 marks</p>