			STEL		
COMP	UTER SCIENCE FEDERAL PUB COMPETIT RECRUITMENT THE FEDER	LIC SERVICE COMMISSIO IVE EXAMINATION FOR I TO POSTS IN BPS-17 UND RAL GOVERNMENT, 2009	N ER S.No.		
	<u>COMPUTER SCIENCE</u>		R.No.		
FIME A	ALLOWED: (PART-I) 30 (PART-II) 2	MINUTES HOURS & 30 MINUTES	MAXIMUM MARKS:20 MAXIMUM MARKS:80		
NOTE	: (i) First attempt PART-I after 30 minutes.	(MCQ) on separate Answer Sh	eet which shall be taken back		
	(11) Overwriting/cutting o	t the options/answers will not	be given credit.		
		$\frac{PART - I(MCQ)}{(COMPLU SORV)}$			
<b>)</b> 1	Select the best ontion/answe	r and fill in the appropriate bo	y on the Answer Sheet (20)		
(i)	AX register is also known as:	and in in the appropriate bo	x on the Answer Sheet. (20)		
~ /	(a) Accumulator	(b) Collector	(c) Distributor		
	(d) Counter	(e) None of these			
(ii)	In the Base + Offset addressin	g, Offset address is also known a	as:		
	(a) Physical Address	(b) Logical Address	(c) Actual Address		
(jij)	(u) instruction Address The technique for allowing a	unit to check the status of and	ther independently function unit is		
(111)	known as?	unit to cheek the status of all	saler independently function unit is		
	(a) Interrupt	(b) System call	(c) Polling		
	(d) Trape	(e) None of these			
(iv)	The method for storing data to	emporarily such that the input-o	utput of the same job is overlapped		
	with its own processing, is known of the second sec	Own as: (b) Contantion	(a) I/O wrait		
	(d) Buffering	(b) Contention (c) None of these			
(v)	The DMA that uses Busses wh	ten CPU is not using them is ter	med as:		
	(a) Shared DMA	(b) Cycle Stealing	(c) Channel		
	(d) Transparent DMA	(e) None of these			
(vi)	Scheduler deals	with the decision as to whethe	r to admit another new job to the		
	(a) High Level	(b) Medium Level	(c) I ow I evel		
	(d) Short term	(e) None of these			
(vii)	When the process is in the sta	ates of Blocked Suspended or R	eady Suspended, its relevant data is		
	stored in:	•			
	(a) Main Memory	(b) Hard Disk	(c) Magnetic Tape		
(	(d) Buffer Driority D (There W) it	(e) None of these	to coloulate enjegite in		
(V111)	scheduling algorithm:				
	(a) Shortest Job First	(b) Priority Scheduling	(c) Longest Wait First		
	(d) Highest Response Ratio N	ext (e) None of these			
(ix)	HDLC Protocol stands for:				
	(a) High-Level Data Link C	ontrol (b) High Level	Development Protocol		
	(c) Huge Level Data Link C	ontrol (d) High Devel	opment Level Control		
( <b>x</b> )	(c) INORE OF THESE	e for a set of standards issued b	v the International Communications		
(X)	Standards Body CCITT, designed to support Message Handling Systems: i.e. Electronic Mail				
	(a) TCP/IP	(b) ISDN	(c) X.400		
	(d) X.25	(e) None of these			
(xi)	layer is responsible	for the transfer of a packet of d	ata along one link in the network. It		
	organizes data into frames and	detects errors in transmission.			
	(a) Physical Layer (d) Transport Layer	(b) Data Link Layer	(c) Network Layer		
(xii)	(u) Transport Layer Encryption is the conversion	(e) INOILE OI THESE of data in some intelligible for	ormat into an unintelligible format		
	called to prevent the data from being understood if read by an unauthorized party				
	(a) Clear Text	(b) Encrypted Text	(c) Cipher Text		
	(d) Coded Text	(e) None of these	· · · <b>-</b>		
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COMP	UTER SCIENCE	
(viii)	Binary search requires about comparisons with an initial list of 1,000,000 et	
(XIII)	(a) $10$ (b) $20$ (c) $35$ (d) $100$ (e) None of the	En
(xiv)	A header list is a header list where the last node contains the null pointer.	2
(xv)	are small applications that are accessed on an internet server, transported ov	ver the
	internet, automatically installed and run as part of a web document.	
(xvi)	(a) Applets (b) Java Bean (c) Sockets (d) Java Component (e) None o AWT stands for:	t these
(11)	(a) Abstract Window Technique (b) Abstract Window Toolkit (c) Actual Window Techn	ique
(vvii)	(d) Added Window Toolkit (e) None of these	
	(a) $128$ (b) $256$ (c) $512$ (d) $1024$ (e) None of t	hese
(xviii)	) is stored on a client and contains state information of the website visited.	
(xix)	(a) Cookies (b) Servelet (c) History (d) Resident Page (e) None of t In software Engineering KPA denotes	hese
()	(a) Key Process Audit (b) Key Process Area (c) Key Process Analysis	
( <b>vv</b> )	(d) Key Problem Area (e) None of these The Process Model defines a series of events that will trigger transitions from a	tate to
(XX)	state for each of software engineering activities.	
	(a) Spiral (b) Operational (c) RAD	
	(d) Concurrent Development (e) None of these	
	<u>PART – 11</u>	
NOTE:	<ul> <li>(ii) Attempt ONLY FOUR questions from PART-II, selecting at least ONE questi from each SECTION. All questions carry EQUAL marks.</li> <li>(iii) Extra attempt of any question or any part of the attempted question will not considered.</li> </ul>	on be
	<u>SECTION – I</u>	
Q.2. Ex	xplain ANY FOUR.	(20)
(i) (iv	v) Buses & their types (v) Segment Registers (vi) Instruction Pipelining	
<b>0.3.</b> (a)	) Describe briefly five state process lifecycle	(6)
(b	) Explain multi level feedback queue scheduling algorithm.	(6)
(c)	) Describe briefly different communication channels	(8)
	<u>SECTION - II</u>	
<b>Q.4.</b> (a)	) What are Virtual Functions? And how they can be utilized for polymorphism?	(10)
(b	<ul> <li>Explain with examples ANY TWO:</li> </ul>	(10)
	(i) Inheritance & Aggregation (ii) Data Hiding & Encapsulation	
<b>. .</b> ()	(11) Constructors & Destructors (1v) Class, Object and Abstraction	
<b>Q.5.</b> (a)	) Write and explain algorithm for Binary Search. ) Explain <b>ANY TRHEE</b> .	(8) (12)
	(i) Stack & Queue (ii) Tree & Graph (iii) Linked List & Array	(12)
	(iv) Algorithm & Program (v) Complexity of Algorithm	
<b>Q.6.</b> (a)	) Explain the terminologies of Process, Methods and Tools.	(6)
(b	) What is Software Process Model? Explain Spiral Process Model.	(14)
	<u>SECTION – III</u>	
<b>Q.7.</b> (a)	) What is a Database? Explain and differentiate Relational Database Model from the other Database Models.	(10)
(b	) Explain with example Entity Relationship Diagram.	(10)
<b>0.8.</b> Ex	xplain ANY FOUR:	(20)
(i)	Computer Graphics (ii) Pixel Art (iii) Vector Graphics	()
(iv	v) Computer Animation(v) Rendering(vi) 2D & 3D Graphics	
(iv	v) Computer Animation (v) Rendering (vi) 2D & 3D Graphics	

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