



Electronics

Advanced Subsidiary GCE

Unit F611: Simple Systems

Mark Scheme for June 2011

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Mark Scheme

Ques	stion	Grade	Expect	ted answ	ver				Mark	Additional Guidance
1	(a)				-	-	-			
				В	Α	С	D	Q		
				0	0	1	0	0		
				0	1	1	0	0		
				1	0	0	0	1		
				1	1	0	1	0		
		Е	Colum	n C corre	ct				[1]	
		Е	Coumn	D correct	ct				[1]	
		Е	Colum	n Q corre	ect (ecf fr	om C AN	DD)		[1]	
1	(b)	D	$Q = \overline{A}$	·B (ecf)					[1]	Do not accept answers in terms of C and D
1	(c)	E	-)-	-				[1]	
1	(d)	E)-f	Ð	┝			[1]	

Ques	stion	Grade	Expected answer		Additional Guidance
1	(e)	С		[1]	
1	(f)	С	Correct diagram (ecf from c,d & e)	[1]	
		E	Labels correct on correct diagram		
1	(g)	В	Any one of: often need fewer integrated circuits takes up less space on the board making more of one i.c. makes it cheaper (owtte)	[1]	

Question		Grade	Expected answer	Mark	Additional Guidance	
2	(a)	(i)	Е	2.2k Ω and 4.7k Ω resistors	[1]	
			E	resistors in series	[1]	
2	(a)	(ii)	A		[1]	
2	(b)		С	$I = \frac{15}{(2.2 \times 10^3 + 5 \times 10^3)} = 2.08 \text{mA}$	[1]	(Calculation of current by) dividing by sum of 2.2k and 5k
			D	$(2.2 \times 10^{-3} - 40^3)$	[1]	Multiply (current) by 5k (ecf)
			С	$V = 2.08 \times 10^{-3} \times 5 \times 10^{3}$ ecf	[1]	Correct answer full marks by any method
				=10.42V		
2	(c)		A	No current flow into or out of the inputs of an op-amp (wtte)	[1]	Op-amp has high input impedance/resistance
2	(d)		А	V=15-4.5=10.5V	[1]	1 mark for dividing a voltage by 30mA correctly
			Е	R=10.5/0.03=350Ω	[1]	accept $340\Omega - 360\Omega$
2	(e)		E		[1]	
2	(f)		B D	The op-amp can only provide 10mA at its output (wtte) The MOSFET can conduct enough current for the LED (wtte)	[1] [1]	Must state that current from op-amp is small (<30mA) Any statement about higher current from MOSFET or using the term "buffer" or "driver"

Question		1	Grade	Expected answer		Additional Guidance
2	(g)		E		[1]	Must have correct symbol and both connections
2	(h)		E D C B	no current through LED because MOSFET off C saturated negative voltage at B > A when cold thermistor has high resistance voltage at B is large/high any of above to max of 4	[max 4]	Allow C=-13V
2	(i)		C A B	the LED will come on (suddenly) as temp rises thermistor resistance falls and V_B falls as soon as B <a (suddenly)="" c="+13V<br">MOSFET conducting as V_{GS}>V_{th} any of above to max 3	[max 3]	Do not allow slowly/gradually LED comes on at particular temp Allow C goes high

Que	stion		Grade	Expected Answer	Mark	Additional Guidance
3	(a)	(i)	Е	$22 \times 10^3 \times 47 \times 10^{-6} = 1.03s$		7.3×10^{n} for 1 mark
			D	correct unit conversions	[1]	
				multiply R by C	[1]	
3	(a)	(ii)	С	$0.7\tau = 0.7s$ (ecf)	[1]	
3	(b)		E D E C	Sudden change at switch pressed 5V between switch pressed and switch released voltage steady	[1] [1] [1] [1]	
				Exponential (by eye) decay from switch released		
3	(c)		E E	C/V 4 2 0 0 1 1 2 5 5 5 5 5 5 5 5 5 5 5 5 5	[1] [1]	
			А	Starts high, goes low then returns high	[1]	
				Changes state when switch pressed Changes state at t=2.1s (by eye) ecf from 3a(ii)		
3	(d)		_	1 mark for each point (max 6 marks):	[6]	
			E	when switch pressed capacitor (instantly) charges to 5V		Do not allow slowly/gradually
				(A=5V makes) Q go to UV		
			B	buzzer continues to sound whilst switch pressed		
			Δ	when switch released C discharges (through R)		
			A	when X reaches 2.5V $Q=5V$		
				buzzer sounds (for 0.7s) after switch released		

Question		Grade	Expected Answer	Mark	Additional Guidance	
4	(a)		E	$T = \frac{1}{f} = \frac{1}{440} = 2.3 \times 10^{-3} s = 2.3 ms$	[1]	
4	(b)		ЕD	$R \ge 10k\Omega$	[1] [1]	mark for large resistor value $BC = 4.5 \times 10^{n}$ for 1 mark
			С	$RC = \frac{2.3 \times 10^{-5}}{0.5}$ (correct equations)	[1]	Allow calculation using T=2ms
				$= 4.6 \times 10^{-3}$ s e.g. R = 10k Ω C = 4.5 × 10 ⁻⁷ F		
4	(c)		Е	connection of some symbol for CRO between A and 0V	[1]	
4	(d)		E D C	square wave 2.5 divisions high period 4 division	[1] [1] [1]	position of trace on screen unimportant

Question		Grade	Expected Answer	Mark	Additional Guidance	
4	(e)					
			Е	switch pulls AND gate input high	[1]	
			D	resistor in series with switch between supply rails	[1]	
4	(f)		D	$I = \frac{V}{R} = \frac{5}{8} = 0.625A$	[1]	
4	(g)	(i)	Е	ZVN4306A	[1]	
4	(g)	(ii)	E	EITHER can conduct sufficient current OR low R _{DS} on	[1]	

Question		Grade	Expected Answer	Mark	Additional Guidance	
5	(a)		E	1 mark for each correct box	[4]	
5	(b)		А	The flow of information	[1]	
5	(c)	(i)	E	× × ×	[1]	Allow any clear identification of LDR
5	(c)	(ii)	E	Resistance dependes on light intensity	[1]	
			<u>D</u>	Resistance falls with increased light intensity	[1]	
5	(c)	(111)	D C C	current 0 for negative V current 0 then rises sharply at about 0.7V (by eye)	[1] [1] [1]	Not more than 2V for rise
5	(c)	(iv)	A A A	$I = \frac{5}{22 \times 10^{3}} = 2.27 \times 10^{-4} \text{ A}$ $V_{R} = 13 - 0.7 - 5 = 7.3 \text{ V}$ $R = \frac{7.3}{2.27 \times 10^{-4}} = 3.2 \times 10^{4} \Omega = 32 \text{ k} \Omega$	[1] [1] [1]	Beware of missing 0.7V (lose 1 mak for wrong V) ecf
5	(c)	(v)	D	Potentiometer	[1]	Not variable resistor
5	(c)	(vi)	A B	allows the voltage at the inverting input to be altered to adjust the turn on/turn off (light level)	[1] [1]	

Question		Grade	Expected Answer	Mark	Additional Guidance	
6	(a)		С	$W = \overline{C} \cdot \overline{B} \cdot A + \overline{C} \cdot B \cdot A + C \cdot B \cdot \overline{A} + C \cdot B \cdot A$	[1]	
6	(b)	(i)	В	$W = \overline{(\overline{A \cdot \overline{C}})} \cdot \overline{(\overline{B \cdot C})} \text{ from circuit}$	[1]	1 mark for getting correct expression of diag
			A	$W = C \cdot B \cdot A + C \cdot B \cdot A + C \cdot B \cdot A + C \cdot B \cdot A$ $W = \overline{C} \cdot (B + \overline{B}) \cdot A + C \cdot B \cdot (A + \overline{A}) \text{factorizing}$	[1]	1 mark for any correct Boolean manipulation 1 mark for any other type of Boolean manipulation
			A	$W = \overline{\overline{C} \cdot A + C \cdot B} [A + \overline{A}]$	[1]	
				$W = \overline{(A \cdot \overline{C})} \cdot \overline{(B \cdot C)} D.M.T.$		
6	(b)	(ii)	В	to improve clarity of circuit diagram (owtte)	[1]	Do not accept 'unnecessary'
6	(c)		В	Z 1 0 1 0 0 0 1 0 1 0 1 0 1 0 1 0 1 0 1 0 1 0 1 0 0 0	[1]	

Q	Question		Grade	Expected Answer	Mark	Additional Guidance
6	(d)					
			С	correct inversion of each line	[1]	
			В	use of 2 input AND for A&C and 3 input AND for others	[1]	alternatively use two 2-input AND gates to make
			E	use of OR gate for Z	[1]	3-input AND gate

Quality of Written Communication

- 3 The candidate expresses complex ideas extremely clearly and fluently. Sentences and paragraphs follow on from one another smoothly and logically. Arguments are consistently relevant and well structured. There will be few, if any, errors of grammar, punctuation and spelling.
- 2 The candidate expresses straightforward ideas clearly, if not always fluently. Sentences and paragraphs may not always be well connected. Arguments may sometimes stray from the point or be weakly presented. There may be some errors of grammar, punctuation and spelling, but not such as to suggest a weakness in these areas.
- 1 The candidate expresses simple ideas clearly, but may be imprecise and awkward in dealing with complex or subtle concepts. Arguments may be of doubtful relevance or obscurely presented. Errors in grammar, punctuation and spelling may be noticeable and intrusive, suggesting weaknesses in these areas.
- **0** The language has no rewardable features.

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