

GCE AS and A Level

# **Electronics**

AS exams 2009 onwards A2 exams 2010 onwards

## Unit 4: ELEC4 Specimen mark scheme

Version 1.1

The specimen assessment materials are provided to give centres a reasonable idea of the general shape and character of the planned question papers and mark schemes in advance of the first operational exams.

For operational papers, mark schemes are prepared by the Principal Examiner and considered, together with the relevant questions, by a panel of subject teachers. The mark scheme includes any amendments made at the standardisation meeting attended by all examiners and is the scheme which was used by them in this examination. The standardisation meeting ensures that the mark scheme covers the candidates' responses to questions and that every examiner understands and applies it in the same correct way. As preparation for the standardisation meeting each examiner analyses a number of candidates' scripts: alternative answers not already covered by the mark scheme are discussed at the meeting and legislated for. If, after this meeting, examiners encounter unusual answers which have not been discussed at the meeting they are required to refer these to the Principal Examiner.

It must be stressed that a mark scheme is a working document, in many cases further developed and expanded on the basis of candidates' reactions to a particular paper. Assumptions about future mark schemes on the basis of one year's document should be avoided; whilst the guiding principles of assessment remain constant, details will change, depending on the content of a particular examination paper.

Further copies of this Mark Scheme are available to download from the AQA Website: www.aqa.org.uk

Copyright © 2007 AQA and its licensors. All rights reserved.

#### COPYRIGHT

AQA retains the copyright on all its publications. However, registered centres for AQA are permitted to copy material from this booklet for their own internal use, with the following important exception: AQA cannot give permission to centres to photocopy any material that is acknowledged to a third party even for internal use within the centre.

Set and published by the Assessment and Qualifications Alliance.

### **Mark Scheme**

1	(a)	(i)	A separate program instruction bus ✓ (Harvard rather than von Neumann)	
		(ii)	Cost effective plus reason ✓ Adaptable plus example ✓	(3 marks)
	(b)	Both need sensors for monitoring since the input conditions can vary, (e.g. temperature of inlet water, quantity of clothes etc) $\checkmark$ Since end point is monitored, the system is closed. $\checkmark$		
	(c)	(i)	When a portion of the output is mixed with the input $\checkmark$ to reduce the effect of the input $\checkmark$	
		(ii)	If the speed is too low, then the output from the speed sensor is reduced so allowing the input to increase, increasing the speed of the drum and vice versa.	(3 marks)
	(d)	(i)	Comparator 🗸	
		(ii)	The microcontroller produces a reference voltage depending on what temperature setting has been made. $\checkmark$ The output voltage from the thermistor voltage divider is compared with this voltage and when it exceeds the reference voltage the output of the op-amp goes low indicating to the microcontroller that the temperature has been reached. $\checkmark$ <i>(question total)</i>	(3 marks) 11 marks)
2	(a)	(i)	$D_7$ to top (10k) resistor, $D_0$ to the bottom resistor $\checkmark$	
		(ii)	Working down, 160k, 320k, 640k and 1280k ✓ ✓	(3 marks)
	(b)	Op-amp has a very large voltage gain and so for any non-saturated output voltage the voltage on the two input terminals of the op-amp must be virtually the same $\checkmark$ Since the V+ input terminal is at 0V, then P must be virtually at 0V $\checkmark$		
	(c)	(i)	01010000 🗸	
		(ii)	$V_{out} = -10\left(\frac{5}{20} + \frac{5}{80}\right) = 3.125V  \checkmark  \checkmark  \checkmark$	(4 marks)





<sup>(2</sup> marks) (question total 11 marks)

3	(a)	Restore logic levels and make transitions between logic levels fast $\checkmark$ Remove noise $\checkmark$		(2 marks)
	(b)	(i)	When the output is at 0V, the voltage divider connected to point A consists of a $47k\Omega$ resistor at the top and two $47k\Omega$ resistors in parallel at the bottom $\checkmark$ Combined resistance at bottom is $23.5k\Omega \checkmark$ Voltage divided into ratio of $2:1 =>$ voltage at point A = 4V $\checkmark$	
		(ii)	When output is 12V, there are two $47k\Omega$ resistors at the top of the voltage divider and one $47k\Omega$ resistor at the bottom $\checkmark$ Combined resistance at top is $23.5k\Omega \checkmark$ Voltage divided into ratio of 1:2 => voltage at point A = 8V $\checkmark$	
		(iii)	Op-amp has a very large open loop voltage gain so acts as a comparator $\checkmark$ When the input rises above 8V, the inverting input to the op-amp is greater than the non-inverting input, so the output goes to $0V \checkmark$ Similarly when the input voltage goes below 4V when the output is at $12V \checkmark$ (question tot)	(9 marks) al 11 marks)
4	(a)	Advantages - e.g. larger range of characters ✓ Disadvantages - e.g. harder to drive, dimmer ✓		(2 marks)
	(b)	R2 positive with respect to C2 $\checkmark$ by the forward voltage of the LED $\checkmark$ mention of series resistor $\checkmark$		(3 marks)
	(c)	(i)	Interrupt - the microcontroller continues with other tasks until the external device indicates that it needs attention. The microcontroller suspends its current operation, deals with the Interrupt request and then resumes its previous task. With polling - the microcontroller is not able to do any other tasks $\checkmark$	

### (ii) XXX11110, XXX11101, XXX11011, XXX10111, XXX01111 ✓ ✓

- (d) X1111100, X0001010, X0001001, X0001010, X1111100 ✓✓ (2 marks) (question total 11 marks)
- 5 (a) Prevents damage to the semiconductors ✓ From the large induced voltage when a motor coil switches off ✓ (2 marks)

(b)		
INPUT A	INPUT B	MOTOR OPERATION
0	0	Off
0	1	Clockwise
1	0	Anticlockwise
1	1	Off

 $\checkmark$   $\checkmark$   $\checkmark$   $\checkmark$ 

(c) (i) 
$$\mathbf{A} = (\mathbf{SG}) \bullet (\overline{\mathbf{FR}}) \checkmark$$
  
 $\mathbf{B} = (\mathbf{SG}) \bullet (\mathbf{FR}) \checkmark$ 

(4 marks)

(4 marks)



6

(a)

 (i) Inclusion of:-Simple - Photodiode/LDR Multiple receptor sensors e.g. CCDs Use of IR/Visible/UV colour etc, Ultrasound, microwaves etc Or other relevant vision systems/techniques ✓ ✓ ✓ ✓

	(ii)	ANN trained with known shapes and patterns. Then given unknown shapes/patterns to interpret. Backwards propagation. Or other relevant techniques $\checkmark \checkmark \checkmark \checkmark \checkmark$	(8 marks)			
(b)	Inclusion of:- NiCd, NiMH, LiIon, Pb acid etc. Relevant advantages and disadvantages Or other relevant batteries $\checkmark \checkmark \checkmark \checkmark$ (question total)					
(a)	Any re e.g. St Conver Needs conver	elevant statements sepper motor – rotates through specified angle ✓ ntional motor – rotates continuously when current applied ✓ shaft encoder or encoded disk to provide control of a ntional motor ✓	(3 marks)			
(b)	Statem Releva E.g. S needs l extra e E.g. C resolut Or neg	ent of recommendation $\checkmark$ int justification $\checkmark$ tepper – readily apply to situation; direct control by microcontroller; little additional control equipment; accurate rotation with little quipment conventional – cheaper; greater power/torque; could provide greater ion atives $\checkmark \checkmark$	(3 marks)			
(c)	For ou	tputs a bit is 0 and for inputs a bit is 1 => byte is F0h $\checkmark$	(1 mark)			
(d)	Use pro This gi Timing	escaler to divide by 250 so load it with 250. ives 250µs pulses ✓ ✓ g register needs 200 of these pulses for 50ms so load with 200 ✓	(3 marks)			
(e)	(i)	Loads the value in register 05h (PORT A) into the Working register $\checkmark$				
	(ii)	Masks bit 5 from other possible inputs and outputs $\checkmark$				
	(iii)	If the zero flag is set, then bit 5 is a zero $\checkmark$ test-tube A is in line with the liquid supply tube $\checkmark$ (question total)	(4 marks) 14 marks)			