| Centre Number | | | Candidate Number | | |
|---------------------|--|--|------------------|--|--|
| Surname | | | | | |
| Other Names | | | | | |
| Candidate Signature | | | | | |



General Certificate of Education Advanced Level Examination June 2010

Electronics

ELEC4

Unit 4 Programmable Control Systems

Tuesday 8 June 2010 9.00 am to 10.30 am

For this paper you must have:

- a pencil and ruler
- a calculator
- a Data Sheet.

Time allowed

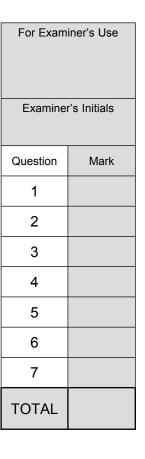
• 1 hour 30 minutes

Instructions

- Use black ink or black ball-point pen.
- Fill in the boxes at the top of this page.
- Answer all questions.
- You must answer the questions in the spaces provided. Do not write outside the box around each page or on blank pages.
- Do all rough work in this book. Cross through any work you do not want to be marked.

Information

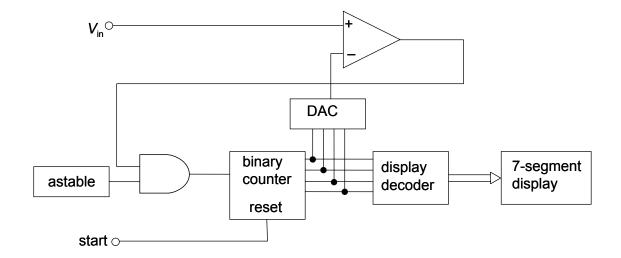
- The marks for questions are shown in brackets.
- The maximum mark for this paper is 80.





Answer all questions in the spaces provided.

The block diagram of a digital voltmeter containing a digital ramp ADC connected to a 7-segment display via a decoder is shown below. The voltage being measured is $V_{\rm in}$.



- 1 (a) What is meant by the terms:
- 1 (a) (i) ADC

| | | |
|------|------|----------|
| | | (1 mark) |

1 (a) (ii) DAC?

| (1 | mark) |
|----|-------|

1 (b) Describe the function of the display decoder in this circuit.

| |
|---------------|
| |
| (2 marks) |



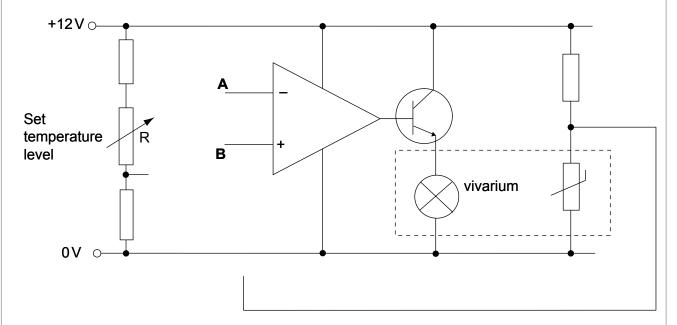
| 1 | (c) | The conversion begins when the start signal causes the binary counter to reset to zero. State how the voltage at the output of the DAC varies with time after this reset occurs. |
|---|-----|--|
| | | (1 mark) |
| 1 | (d) | Explain the sequence of operations of this digital ramp ADC, referring to the subsystems in the block diagram on page 2 . |
| | | |
| | | |
| | | |
| | | |
| | | (4 marks) |

9

Turn over for the next question



A student has a pet corn snake that is housed in a vivarium. It is important that the snake's environment is kept at a constant temperature of about 31°C. The student decides to design a temperature control system using a lamp, beneath the gravel, as the heat source, and a thermistor as the temperature sensor. She designs the system shown below.



2 (a) Complete the circuit diagram by connecting the input and feedback signals to the correct points (A and B) on the circuit.

(2 marks)

| 2 | (b) | State whether this system is a closed loop control system or an open loop control system, and give a reason. | |
|---|-----|--|--------|
| | | | |
| | | | |
| | | | |
| | | (2 n | narks) |

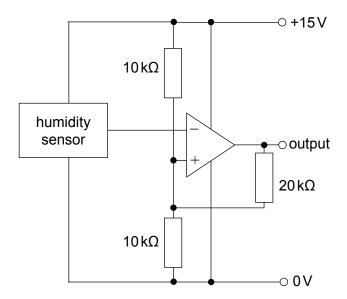


| 2 | (c) | When the system has stabilized at a particular temperature, the resistance of R is then suddenly increased. Explain, with reference to the circuit diagram on page 4 , how the system behaves as it reaches a new stable temperature. |
|---|-----|---|
| | | |
| | | |
| | | |
| | | |
| | | |
| | | (5 marks) |

Turn over for the next question



An extractor fan for a bathroom has a built in humidity sensor. Once activated, the fan will continue to operate until the humidity of the air drops below a preset value. The Schmitt trigger part of the circuit diagram is shown below.

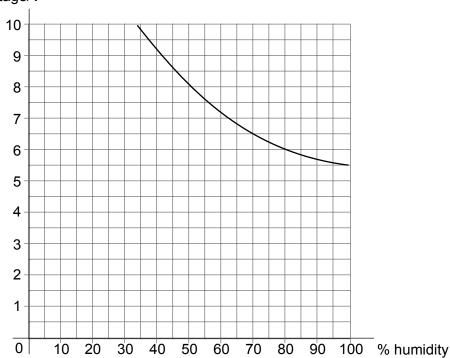


| 3 | (a) | Show that the lower switching level is approximately 6 V. |
|---|-----|---|
| | | |
| | | |
| | | |
| | | (3 marks) |
| 3 | (b) | Calculate the upper switching voltage. |
| | | |
| | | |
| | | |
| | | (3 marks) |



3 (c) The graph of output voltage against % humidity for the humidity sensor is shown below.

> sensor output voltage/V



(c) (i) At what % humidity will the output of the op-amp become 15 V?

(1 mark)

(c) (ii) Explain how you arrived at your answer.

(2 marks)

| 4 | | An electronic weighing machine is to have a multiplexed four digit 7-segn | nent display. |
|---|----------|---|---------------|
| 4 | (a) (i) | Explain the meaning of the word multiplexed in this context. | |
| | | | |
| | | | (4 |
| | (-) (ii) | | (1 mark) |
| 4 | (a) (ii) | Describe one advantage and one disadvantage of using multiplexed disp | olays. |
| | | | |
| | | | |
| | | | |
| | | | (2 marks) |
| 4 | (b) | Multiplexed displays can be either LED or LCD. | |
| | | Describe one advantage and one disadvantage of LED displays compare displays. | ed with LCD |
| | | | |
| | | | |
| | | | |
| | | | |
| | | | (2 marks) |
| 4 | (c) | The circuit diagram for such an LED display is shown below. | |
| | | M D ₇ | |
| | | | |
| | | $ \begin{array}{cccccccccccccccccccccccccccccccccccc$ | |
| | | $ \begin{array}{cccccccccccccccccccccccccccccccccccc$ | |
| | | $\begin{array}{cccccccccccccccccccccccccccccccccccc$ | |
| | | T D ₀ D ₀ O | |
| | | | |
| | | R D ₁ | |
| | | | - 0)/ |
| | | | ○ 0 V |

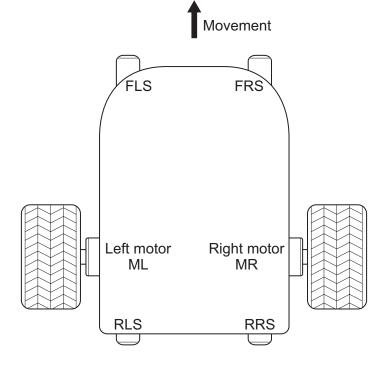


| 4 | (c) | Why is a common cathode display needed in this circuit? | |
|---|----------|---|---------|
| | | (1 mark | · () |
| 4 | (d) (i) | The maximum output voltage from port A of the microcontroller is 5V. Each segment in the 7-segment displays has a forward voltage of 1.9V and the saturated collector-emitter voltage of the transistors is 0.1V. The maximum power dissipation of each segment is 76 mW. Calculate a suitable value for R. | |
| | | | |
| | | | |
| | | (5 marks | ;) |
| 4 | (d) (ii) | Estimate the peak current that each transistor must be able to switch. | |
| | | | |
| | | | |
| 4 | (e) | (2 marks) If the display is to be scanned from left to right, state the sequence of numbers which must be sent to port B. | 5) |
| | | (2 marks | s) |
| | | | 1 |



A robot, moving forward, receives a signal from its front left sensor (FLS) that there is an obstacle ahead.

Obstacle



| (a) | Identify two possible devices that could be used to detect an obstacle and describe the operation of one of these. | briefly |
|-----|--|-----------|
| | | |
| | | |
| | | |
| | | (3 marks) |
| | (a) | |

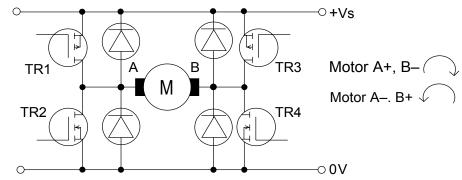


| direction. Describe a sequence of control commands that must be sent to the motors in order for the robot to accomplish this. |
|--|
| |
| |
| |
| |
| |
| (4 marks) |
| State two different technologies used in rechargeable batteries and discuss their relative merits. |
| |
| |
| |
| |
| |
| |
| |
| (4 marks) |

Turn over for the next question



| 6 | (a) | Name the arrangement of MOSFETs in the circuit below. | |
|---|-----|---|----------|
| | | | (1 mark) |



| 0 | (D) | application. | nis |
|---|-------------|-------------------------------------|-----------|
| | | | |
| | | | |
| | | | |
| | | | (4 marks) |
| 6 | (c) | Explain the function of the diodes. | |
| | | | |
| | | | (1 mark) |

6 (d) Complete the table below to show the logic state of the gates of the MOSFETs for the given directions of the motor.

| Motor direction | TR1 | TR2 | TR3 | TR4 |
|-----------------|-----|-----|-----|-----|
| | | | | |
| | | | | |
| Stop | | | | |

(4 marks)

10



| 7 | (a) | Descr micro | | | - | which | the a | archite | ecture | of a r | micro | contro | ller d | iffers f | rom t | hat of | (2 marks) is D ₀ 1 out in (3 marks) |
|---|-------|--|----|-----|----------------|-------|----------------|---------|-----------------------|--------|-------|--------|-----------------------|----------|----------------|--------|--|
| | | | | | | | | | | | | | | | | | |
| | | | | | | | | | | | | | | | | (2 m | arks) |
| 7 | (b) | The d logic | | | | | | | | | | | | A to inp | out if | it is | |
| | | D | 7 | С |) ₆ | |) ₅ | |) ₄ | С |)3 | [| D ₂ | |) ₁ | |) ₀ |
| - | TRISA | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 | 0 | 1 |
| | PORTA | out | in | out | in | out | in | out | in | out | in | out | in | out | in | out | in |
| 7 | | What value, in hexadecimal, must be written to TRISA so that D_7 and D_6 are inputs and all of the others are outputs? | | | | | | | and | | | | | | | | |
| | | | | | | Que | stion | 7 cor | ntinue | es on | the n | ext p | age | | | (2 m | arks) |



| 7 | (c) | The following is a <i>subroutine</i> from a microcontroller system. |
|---|----------|---|
| | | start: MOVRW PORTA ANDW 0x80 JPZ start MOVW 2 MOVWR PORTA RET |
| 7 | (c) (i) | Explain what is meant by a subroutine. |
| | | |
| | | (2 marks) |
| 7 | (c) (ii) | Explain the meaning of each of the instructions. |
| | | start: |
| | | |
| | | MOVRW PORTA |
| | | |
| | | ANDW 0x80 |
| | | JPZ start |
| | | or 2 start |
| | | MOVW 2 |
| | | |
| | | MOVWR PORTA |
| | | |
| | | RET |
| | | (7 marks) |
| | | (· |

17

END OF QUESTIONS



