



ASSESSMENT and
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Mark scheme

June 2002

GCE

Electronics

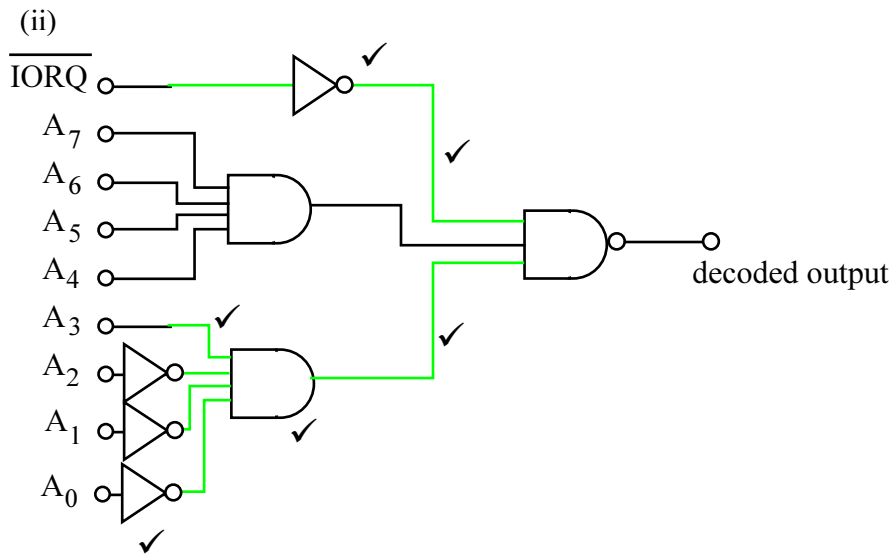
Unit ELE4

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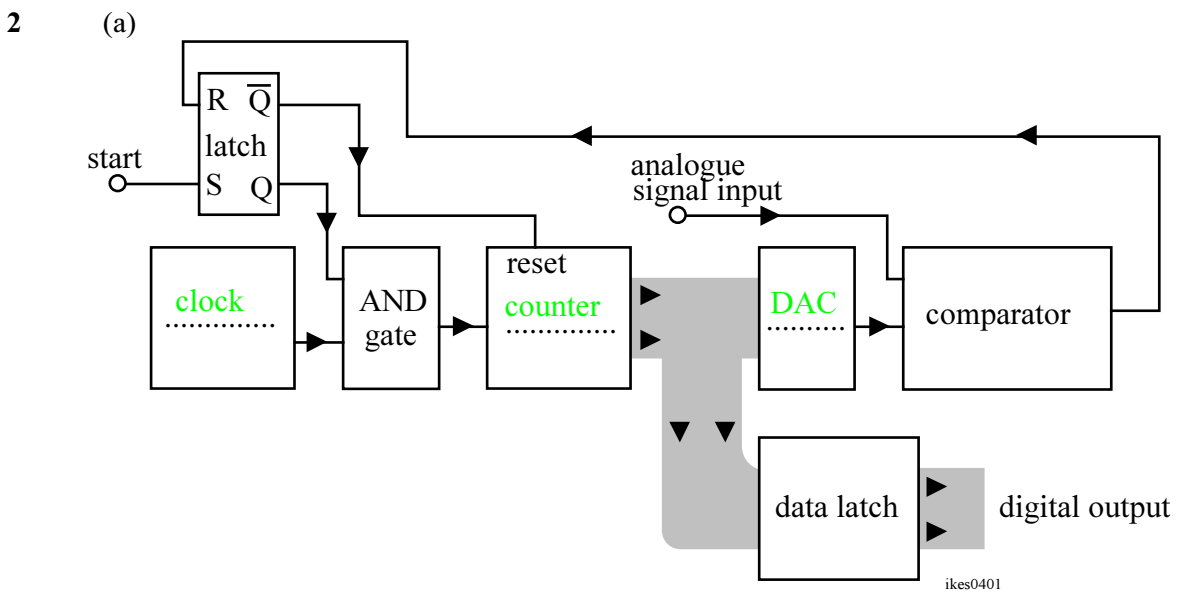
Electronic Control Systems

- 1 (a) (i) data bus ✓
 (ii) clock ✓
 (iii) RAM ✓
 (iv) input port ✓
 (v) address bus ✓ (5)
- (b) (i) Memory mapping is when an I/O port appears as part of the memory, whereas I/O mapping is when the I/O ports are addressed separately to the memory ✓ (1)



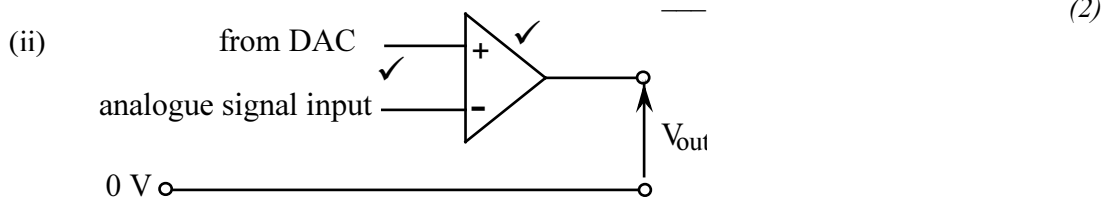
(6 max 4)

(10)



(3)

- (b) (i) op-amp ✓
because it has a very large voltage gain and gives minimal indecision as to which input has the larger voltage. ✓

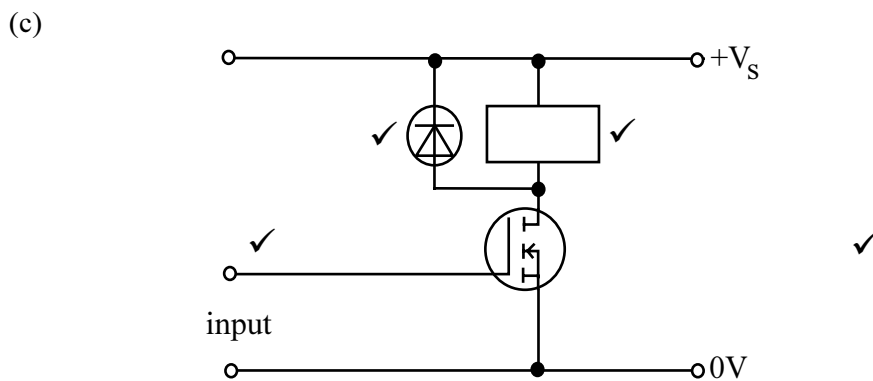


- (c) Flash ADCs give very fast conversion times. This is not needed in this application. ✓
- (1)
(8)

- 3 (a) (i) summing amplifier ✓
- (ii) calculation ✓
-0.3125V ✓
- (iii) 10, 20, 40, 80, 160, 320, 640, 1280kΩ ✓ ✓

- (b) Data latch needed to
- (i) capture the data from the data bus when the data is written to the output port
- (ii) to hold the data steady while the DAC makes the conversion. ✓ ✓
- (1)
(2)
(7)

- 4 (a) significantly cheaper ✓
allows for greater flexibility ✓
mechanical timers have moving parts which wear ✓
- (max 2)
- (b) a PIC contains a microprocessor, ROM, RAM and I/O ports ✓
ie it is a computer fabricated on a single piece of silicon ✓
Do not need external devices to operate ✓
- (max 2)

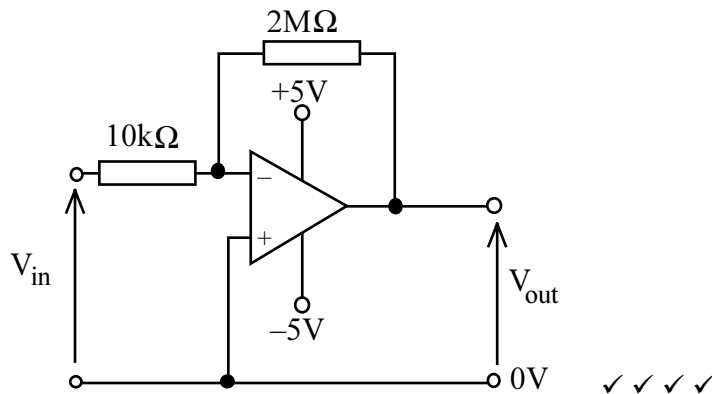


Input to gate and no series resistor, MOSFET symbol correct, relay in drain lead, diode across relay.

(4)
(8)

- 5 (a) (i) The angular position of the armature is directly controlled by the control system so its angular speed is accurately controlled. ✓ (1)
- (ii) it is very expensive to have high power stepper motors ✓
do not normally rotate as fast as 3000rpm ✓ (max 1)
- (b) Optical shaft encoder enables the angular position of an axle to be accurately determined. Usually a photo emitter and photo detector are positioned either side of a disk which has accurately positioned slots. The disk is attached to the axle and rotates with the axle.
Diagram ✓ ✓ ✓ (3)
- (c) (i) Closed loop system ✓
Because the position of the motor axle is fed back to the microprocessor controller via the shaft encoder ✓ (2)
- (ii) Negative feedback ✓
If the motor is rotating too fast the system will slow it down and vice versa ✓ ✓ (3)
- (10)

6 (a)



- (b) (i) 201 ✓ (4)
- (ii) Limited by the power supply voltage which is $\pm 5V$. ✓ (1)
- (c) (i) Multiple crossings of zero volt line as waveform change state so leading to errors ✓ (1)
- (ii) Calculation ✓, $\pm 2.5V$ ✓ (2)
- (iii) The output of the schmitt trigger only changes state when the switching levels have been exceeded ✓
multiple transitions as the output changes state are therefore eliminated. ✓ (2)
- (11)

- 7 (a) (i) Assume output of first NAND gate low, output of astable high ✓
 Capacitor discharges and charges in opposite direction ✓
 Until voltage at input to first NAND gate $< +V_s/2$ ✓
 Output of astable switches state ✓
 Capacitor charges in opposite direction ✓
 Process repeats ✓
- (6 max 5)
- (ii) effective capacitance is 25nF ✓ (1)
- (iii) $f = 1/2RC \Rightarrow$ substitution to get 2kHz ✓ (1)
- (b) Polling - computer repeatedly reads state of port looking for the appropriate change to act upon ✓
 Interrupts - the computer continues with other activities until the required change occurs and then it stops what it was doing and processes the event ✓ (2)
- (c) (i) $A\% = \text{INP}(\&H379)$ read into A% the data on input port &H379 ✓
 $A\% = A\% \text{ AND } X\%$ AND the value in A% with the astable number and restore in A% ✓
 LOOP UNTIL $A\% = X\%$ continue the process until A% is equal to the astable number X% ✓
 $A\% = A\% \text{ XOR } A\%$ XOR A% with itself and restore in A%. Has the effect of setting A% to 0, (Reset A% to 0) ✓ (4)
- (ii) Road C because it is connected to D₂. (If X% is 4 then when ANDed with A% it will mask all other inputs by setting them to 0) ✓ (1)
- (d) (i) A neural network has a large number of simple processing units, which all process a small proportion of the data, but in parallel whereas a conventional computer has a single processing unit through which all data passes (Information stored in neurons) ✓ ✓ (2)
- (ii) A neural network is able to learn and so would be able to control all of the traffic lights to maximise the throughput of traffic so reducing travelling time and pollution. Disadvantage is that if it gets it wrong or fails then the whole city becomes grid-locked. (Cannot back up in case of failure) ✓ ✓ (2)
- (18)