## MARK SCHEME for the October/November 2014 series

## 9691 COMPUTING

9691/31
Paper 3 (Written Paper), maximum raw mark 90

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| Page 2 | Mark Scheme | Syllabus | Paper |
| :---: | :---: | :---: | :---: |
|  | Cambridge International A Level - October/November 2014 | 9691 | 31 |

1 (a) (i) $a b+6 /$
(ii) $\frac{3 \mathrm{xy*3+}}{1}$ *
(b) (i) $3 \times(x+y+z)$
(ii) $\left(7^{y}+6\right) / 2$

1 mark only for:

- $7^{y}$ or...
- $\left(7^{\wedge} y=6\right) / 2$
(c) (i) Last item added is the first to leave // first add will be the last to leave Last in - First out // First in - Last out
Refuse: LIFO
(ii)


2 (a) The main memory is divided into page frames
The program is divided into pages
Only some of the pages of the program are loaded to start execution of the program
The operating system must manage the allocation of pages to page frames
The Page (Map) table shows the mapping of pages to page frames
(b) 'Priority' which is well explained and clear $\times 2$
E.g. Anticipated shortest time to complete

Shortest remaining time to complete

| Page 3 | Mark Scheme | Syllabus | Paper |
| :---: | :---: | :---: | :---: |
|  | Cambridge International A Level - October/November 2014 | 9691 | 31 |

(c) Mark as follows...
$B-D-F-A$
Scores full 4
or...
C and E are excluded 1
B 1
D 1
F 1
A each in the correct position 1
(ii)

$2 X$ correct relationship
(iii) A customer can never purchase more than one painting on the same date
(b) (i) Not in 2NF... - Sales

CustomerName is known from only CustomerID // CustomerName will be known by only knowing part of the primary key 1

Sales (CustomerID, PurchaseDate, PaintingID)
(ii) Not in 3NF... - Painting

There are non-key attributes which are dependent.
Or by example...
DateBorn/DateDied/Nationality are all dependant on ArtistName
Painting(PaintingID, Description, PaintingDate, ArtistName, Price)

Artist(ArtistName, ArtistDateBorn, ArtistDateDied, ArtistNationality)

Mark as follows:
All except ArtistName removed from table Painting
New table Artist
Artist containts at least three of the correct attributes
$\begin{array}{ll}\text { (c) UPDATE Customer } & 1 \\ \text { SET TelNo }=" 0123456789 " & 1\end{array}$

| Page 4 | Mark Scheme | Syllabus | Paper |
| :---: | :---: | :---: | :---: |
|  | Cambridge International A Level - October/November 2014 | 9691 | 31 |

WHERE CustomerID $=$ "065" 1
[3]

4 (a) (i) $\mathrm{ACC}=77$ 1
Show contents of 203 copied to ACC 1
(ii) $\mathrm{ACC}=65$ 1

Show 150 used as a forwarding address 1

Contents of 200 copied to ACC 1
(b) (i) 256 different instructions
(ii) Store the ACC contents
at address 65 // 01000001
(iii) Fewer digits to write // less chance of an error in writing the code // easy conversion to/from binary code
(iv) 1041 hex
(v) LDI 150

| 0 | 0 | 0 | 0 | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 1 | 0 | 1 | 1 | 0 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

Opcode 1
Operand
(vi) LDV 15

| 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

Opcode 1
Operand 1
(vii) True

OUTCH / IN // END or using a good explanation (only) of either
[2]

| Page 5 | Mark Scheme | Syllabus | Paper |
| :---: | :---: | :---: | :---: |
|  | Cambridge International A Level - October/November 2014 | 9691 | 31 |

(c)

| ACC | Location 150 | OUTPUT |
| :---: | :---: | :---: |
| 65 |  | A |
| 200 |  |  |
| 201 | 201 |  |
|  |  |  |
| 76 |  | L |
| 201 |  |  |
| 202 |  |  |
|  |  |  |
| 65 |  |  |
| 202 |  |  |
| 203 |  | M |
| 77 | 203 |  |
| 203 |  |  |
| 204 |  |  |

Mark as shown

5 (a) a single processor
program consists of a sequence of stored instructions 1
instructions + data make up a 'program' 1
are stored in a continuous block of main memory 1
instructions are executed in sequence
(b) 1. The (contents of) the program counter/PC are copied to the Memory Address Register
2. The contents of the Program Counter are incremented
3. Identify the address in the Memory Address Register. Go to this address and copy its contents to the Memory Data Register
4. The (contents of) the Memory Data Register are copied to the Current Instruction Register
(c) (i) Control bus
(ii) read/write
interrupt
reset
clock signal
bus request/bus grant

| Page 6 | Mark Scheme | Syllabus | Paper |
| :---: | :---: | :---: | :---: |
|  | Cambridge International A Level - October/November 2014 | 9691 | 31 |

(d) (i) Case 1 1

The operand number is already held in the CIR 1
(ii) Case 2 1

The instruction is for directed addressing
The address bus is loaded with address 35

6 (a) (i) All the keyboards which make up the syntax of the language 1
A token for each keyword 1
(ii) DECLARE, CONSTANT, CALL, REPEAT (any three)
(iii) A list of all the identifiers used by the program 1

A pointer to where their value is stored in memory 1
(iv) i, Customer, Address, DiscountRate, InitialiseCustomerData (any three...)

(v) Lexical analysis

remove any whitespace from the source file ..... 1
remove any comment statements ..... 1
check for obvious errors in the use of identifiers (names) e.g. they do not exceed 64 characters ..... 1
replace all language keywords with their token (by searching for the appropriate keyword in the keyword table) ..... 1
place an identifier names in the symbol table ..... 1
search for the appropriate identifier in the symbol table - the identifier nameis replaced in the source code by a pointer value
(b) (i) Code optimisation the process of taking the final executable code produced by the compiler and changing it in some way
in order that it will use fewer resources // less memory ..... 1
Refuse: reduced in size
it will execute faster ..... 1
removes redundant code ..... 1
(ii) 203

| Page 7 | Mark Scheme | Syllabus | Paper |
| :---: | :---: | :---: | :---: |
|  | Cambridge International A Level - October/November 2014 | 9691 | 31 |

7 (a) 3 (days)
(b) Error
(c) 2 (months)
(d) Error
(e) Error
(f) Built-in functions are those provided (as a part of the programming language) // accept by example
User defined functions are designed and coded by the programmer

