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CAMBRIDGE INTERNATIONAL EXAMINATIONS GCE Advanced Level

MARK SCHEME for the May/June 2013 series

9691 COMPUTING

9691/33

Paper 3 (Written Paper), maximum raw mark 90

This mark scheme is published as an aid to teachers and candidates, to indicate the requirements of the examination. It shows the basis on which Examiners were instructed to award marks. It does not indicate the details of the discussions that took place at an Examiners' meeting before marking began, which would have considered the acceptability of alternative answers.

Mark schemes should be read in conjunction with the question paper and the Principal Examiner Report for Teachers.

Cambridge will not enter into discussions about these mark schemes.

Cambridge is publishing the mark schemes for the May/June 2013 series for most IGCSE, GCE Advanced Level and Advanced Subsidiary Level components and some Ordinary Level components.



	Page 2		2	Mark Scheme	Syllabus	Paper
_				GCE A LEVEL – May/June 2013	9691	33
1	(a)	(i)	Man	y CLUBs play in many LEAGUEs.		[1]
		(ii)	E-R	diagram		[1]
				CLUB Plays In LEAGUE		
		(iii)				
				CLUBTEAM	LEAGUE	
				nk table drawn k one-to-many relationships		[1] [1]
				mary key in CLUB links to foreign key in link table mary key in LEAGUE links to foreign key in link table		[1] [1]
			No r	mention of foreign keys scores max 1 for final mark poi	ints	
	(b)	(i)	One	CLUB has many PLAYERs		[1]
		(ii)	E-R	diagram		[1]
				CLUB HasRegistered PLAYER		
	(c)		-	ary key of table CLUB - ClubName		[1]
		ivia	tcnes	to ClubName in the PLAYER table		[1]
	(d)			a 'list' of the player names and registration numbers female defenders		[1] [1]
						[Total: 12]
2	(a)	Rul	es/Gr	guage ammar (which describe a high level programming lang ax or <u>structure</u> of all program statements	guage // protocol	specification) [2]
	(b)	(i)		le which is defined in terms of itself Not 'procedure'		[1]
		(ii)	Rule	÷ 6		[1]

(iii)

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Expression	Valid/Invalid	Rules used			
[9]	Valid	Uses all the rules except 3		1 + 1	
[dc]	Invalid	Starts with 73 not used	3 not usedends with 7	1+1	
[w,a]	Valid	Starts with 7all rules used, incl, rule 6 twice	all rules used, incl, rule 6 twice ends with 7	1 + 2	

[Total: 14]

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3 (a) Indexed addressing // LDX

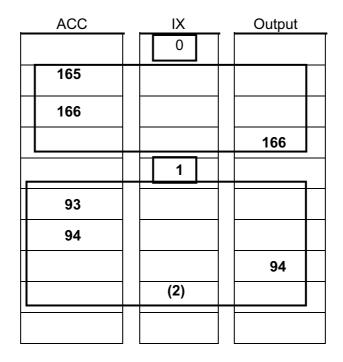
[1]

(b) Indirect addressing
Annotation to explain that address 203 is used as a forwarding address

[2]

(c) 48 [1]

(d)



Mark as follows:

Index register contain 0	[1]
Sequence of first box (or subsequent sequence for the same instructions)	[1]
Index register contains 1	[1]
Sequence for final box	[1]

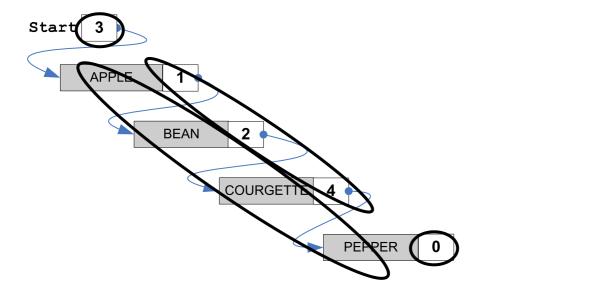
(e) Labels added to a (symbol) table // creates a list of addresses [1]
Labels are later looked up to determine the actual address / Assembler must allocate addresses to labels [1]
Mnemonic looked up to give binary code/machine code [1]
Macro instructions are expanded into a group of instructions [1]
The software makes two passes through the source program [1]
MAX 3

[Total: 11]

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4	(a)	(i)	Dynamic data structure changes <u>size</u> At execution time // A static data structure has a fixed <u>size</u>	[1] [1] [1] MAX 2
		(ii)	Dynamic data structure matches size to data requirements Takes memory from heap as required // returns memory as required (following node deletion) There is no wasted memory space / makes efficient use of memory	[1] [1] [1] MAX 1
	(b)	My]	<pre>MyList[Start].Data = APPLE List[3].Pointer = 1</pre>	[1] [1]

(c)



[4]

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(d) (i) ListTraversal(MyList[Index].Pointer)

[1]

(ii)

ListTraversal(3)
MyList[3].Pointer <> 0 is TRUE

ListTraversal(1)
MyList[1].Pointer <> 0 is TRUE

ListTraversal(2)
MyList[2].Pointer <> 0 is TRUE

ListTraversal(4)

MyList[4].Pointer <> 0 is FALSE
OUTPUT MyList[4].Data // OUTPUT PEPPER
ENDPROCEDURE

OUTPUT COURGETTE ENDPROCEDURE

OUTPUT BEAN ENDPROCEDURE

OUTPUT APPLE ENDPROCEDURE

[4]

[1]

[1]

(iii) The procedure has to backtrack/unwind from the current call

To return to the calling procedure // return to the addresses from which called

MAX 1

[Total: 15]

5 Interpreter translates one instruction, runs it before going on to the next // Compiler (a) – translates all the instructions before run [1] Compiler creates object code/executable file // Interpreter does not [1] Interpreter makes for easier debugging //compiler errors produced away from the execution [1] Compiled programs will execute faster // interpreted code will execute slower [1] Interpreter must be present to run the program // compiler software not needed at runtime [1] Interpreter will translate code in loops more than once // Compiler only once [1] Once compiled no further translation needed // Interpreter needed every program execution attempted [1] MAX 4

Pa	Page 7		Mark Scheme	Syllabus	Paper
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(b)	(i)	all the	keyword table contains: e language keywords/reserved words + with a matching symbol table stores: each identifier/variable found (and its data type) the values of all constants the upper and lower bounds of arrays Mark as: 1 + 1	ng token	[1] [1] [1] MAX 2
	(ii)	Keyw Labe	vords are looked up in the keyword table vords are converted to tokens els are looked up in the symbol table els are converted to actual addresses		[1] [1] [1] MAX 2 [Total: 9]
6 (a)	Bate	ch is)	×		[1]
	All the Pros	There is a time delay before processing All the (data) is processed together/at the same time The payslips are generated as a batch Processing cannot start until all data has been collected/input // all data entered b so processing can be done on the 25 th There is no user involvement			
	The		ve processing is Y) continually wants to see the effect of the changes/des data input by the user	sign produced	[1] [1] MAX 1
(b)	(i)	STAF	FF17 can be loaded		[1]
	(ii)		tion 3 is too small I not allow all 12 students to log-on at 09:00		[1] [1]
	(iii)	devic	rating system // specific modules e.g. interrupt handler be drivers haples of system software or utilities	/scheduler, etc.	[1] [1] [1]
			System software" and "Utilities"		MAX 2
	(iv)	Runr The j	ning job currently has use of the processor		[1] [1]
		the p	pended/Blocked program is unable use the processor/ or by example, the ce : the explanation marks are not dependant on the corr		[1] y using an I/O [1]

90		GCI	A LEVEL – May/June 2013		9691	33	
(c) (i	The	memory is div	ided into fixed sized units calle ided into areas (with the same t is done using a 'page frame t	size) calle	ed page frames	[1] [1]	
	Pag	es will be swap	oped in and out of page frames	s as requir	red.	[1]	
			d disc can act as virtual memo	•		oing) [1]	
	VIII	iai memory ex	ends memory capacity / acts a	as pseudo	-memory	MAX 2	
(ii)) Not	all pages of the	e program need to loaded			[1	
		es memory e jobs can be i	un			[1]	
	11101	e jobs can be i	uii			[1] MAX 1	
						[Total: 16]	
7 (a) (i)) True	e / Yes				[1]	
(ii)) Fals	se / No				[1]	
(iii)) COI	MPILE ERROF				[1]	
(iv) COI	MPILE ERROF				[1]	
(b)							
FUNCT	rion (CalcNetPay	(EmpGrade: CHAR/STRING,	HoursWo	rked:SINGLE, RETURN SING		
	[1]		[41				
			[1] A	: RETURN	[1] REAL/CURREN	- CY	
						[Total: 7]	
(a) Bi	uilding	a model of the	system			[1]	
		del records ove the behaviour o	r time the result of changing parts of the system	arameters	/conditions //	[1]	
			,				
	-		an be written to build the mode can process results very quickl		duce the time fra	[1] ime [1	
			is many changes which are ba			-	
			n various sensors	iseu on ma	atriematicai equa	ations [1 [1	
	•	•	erature / air pressure	- C	.)	[1]	
		parallel proce	ocess results (from many senso ssing	or sources	5)	[1 _] [1	
0	utputs	will be produce	ed which are based on all the a	available d	ata	[1] MAX 4	
						[Total: 6]	

Mark Scheme

Syllabus

Paper

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