



Cambridge International Examinations
Cambridge International Advanced Subsidiary and Advanced Level

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COMPUTER SCIENCE

9608/11

Paper 1 Theory Fundamentals

May/June 2015

1 hour 30 minutes

Candidates answer on the Question Paper.

No Additional Materials are required.

No calculators allowed.

READ THESE INSTRUCTIONS FIRST

Write your Centre number, candidate number and name in the spaces at the top of this page.

Write in dark blue or black pen.

You may use an HB pencil for any diagrams, graphs or rough working.

Do not use staples, paper clips, glue or correction fluid.

DO NOT WRITE IN ANY BARCODES.

Answer **all** questions.

No marks will be awarded for using brand names of software packages or hardware.

At the end of the examination, fasten all your work securely together.

The number of marks is given in brackets [] at the end of each question or part question.

The maximum number of marks is 75.

This document consists of **16** printed pages.

1 (i) Convert the following binary number into hexadecimal.

1 0 1 1 1 0 0 0

.....[1]

(ii) Convert the following denary number into BCD format.

9 7

.....[1]

(iii) Using two's complement, show how the following denary numbers could be stored in an 8-bit register:

114

--	--	--	--	--	--	--	--

- 93

--	--	--	--	--	--	--	--

[2]

2 (a) Sound can be represented in a computer in a digital format.

(i) Give the definition of the term sampling.

.....
.....
.....[1]

(ii) Give **one** reason why 16-bit sampling is used in an audio compact disc (CD).

.....
.....[1]

(iii) Explain what is meant by the term sampling resolution.

.....
.....
.....
.....[2]

(iv) Give **one** benefit and **one** drawback of using a higher sampling resolution.

Benefit

.....

Drawback

.....[2]

(b) Describe **two** typical features found in software for editing sound files.

1

.....

2

.....[2]

(c) Explain the difference between *lossless* and *lossy* data compression techniques.

.....

.....

.....

.....

.....

.....

.....

.....[3]

3 Five modes of addressing and five descriptions are shown below.

Draw a line to connect **each** mode of addressing to its correct description.

Mode of addressing

direct

immediate

indexed

indirect

relative

Description

the operand is the address of the address of the value to be used

the operand is the address of the value to be used

the operand is the offset from the current address where the value to be used is stored

the operand plus the contents of the index register is the address of the value to be used

the operand is the value to be used

[4]

4 (a) Sensors are one type of input device.

For each of the following situations, name a **different** sensor that could be used.

(i) air conditioning in an office building

.....[1]

(ii) maintaining correct growing conditions in a greenhouse

.....[1]

(iii) detecting an intruder in a building

.....[1]

(b) Sensors are used to monitor seismic activity. At the end of each day, all the data are transmitted to a central computer. This is hundreds of kilometres away.

Describe **one** way of ensuring that the integrity of the data is retained during the transmission stage.

.....
.....
.....
.....
.....
.....
.....
.....
.....
.....[4]

(c) Name the hardware device that is being described:

(i) A device that transfers data from one network to another in an intelligent way. It has the task of forwarding data packets to their destination by the most efficient route.

.....[1]

(ii) A device used between two dissimilar LANs. The device is required to convert data packets from one protocol to another.

.....[1]

(iii) A device or software that provides a specific function for computers using a network. The most common examples handle printing, file storage and the delivery of web pages.

.....[1]

6 (a) Name the **most** suitable input or output device for each of the following uses.

Give a different device in **each** case.

Description of use	Input or output device
input of credit card number into an online form	
selection of an option at an airport information kiosk	
output of a single high-quality photograph	
output of several hundred high-quality leaflets	
input of a hard copy image into a computer	

[5]

(b) All of the uses in **part (a)** involve the input or output of data.

(i) Describe **two** methods of preventing accidental loss of data.

- 1
-
- 2
- [2]

(ii) Describe **one** way of ensuring the security of the data against malicious damage.

-
- [1]

- 7 A system is monitored using sensors. The sensors output binary values corresponding to physical conditions, as shown in the table:

Parameter	Description of parameter	Binary value	Description of condition
P	oil pressure	1	pressure \geq 3 bar
		0	pressure $<$ 3 bar
T	temperature	1	temperature \geq 200°C
		0	temperature $<$ 200°C
R	rotation	1	rotation \leq 1000 revs per minute (rpm)
		0	rotation $>$ 1000 revs per minute (rpm)

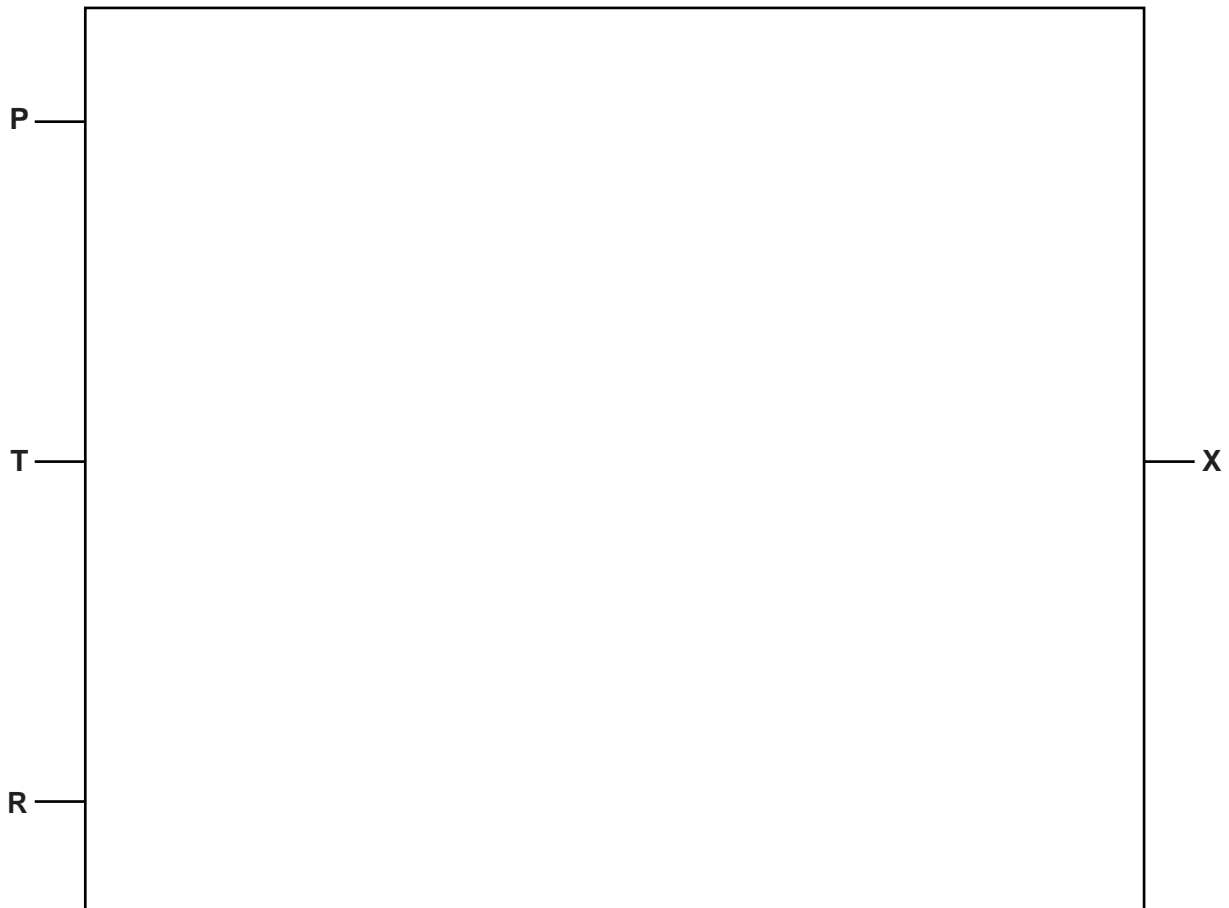
The outputs of the sensors form the inputs to a logic circuit. The output from the circuit, X, is 1 if any of the following three conditions occur:

either oil pressure \geq 3 bar **and** temperature \geq 200°C

or oil pressure $<$ 3 bar **and** rotation $>$ 1000 rpm

or temperature \geq 200°C **and** rotation $>$ 1000 rpm

- (a) Draw a logic circuit to represent the above system.



(b) Complete the truth table for this system.

P	T	R	Workspace	X
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

[4]

- 8 (a) Explain how the width of the data bus and system clock speed affect the performance of a computer system.

Width of the data bus

.....

.....

.....

.....

Clock speed

.....

.....

.....

.....[3]

- (b) Most computers use Universal Serial Bus (USB) ports to allow the attachment of devices.

Describe **two** benefits of using USB ports.

1

.....

2

.....[2]

- (c) The table shows six stages in the von Neumann fetch-execute cycle.

Put the stages into the correct sequence by writing the numbers 1 to 6 in the right hand column.

Description of stage	Sequence number
the instruction is copied from the Memory Data Register (MDR) and placed in the Current Instruction Register (CIR)	
the instruction is executed	
the instruction is decoded	
the address contained in the Program Counter (PC) is copied to the Memory Address Register (MAR)	
the value in the Program Counter (PC) is incremented so that it points to the next instruction to be fetched	
the instruction is copied from the memory location contained in the Memory Address Register (MAR) and is placed in the Memory Data Register (MDR)	

[6]

Question 9 begins on page 14.

9 A database has been designed to store data about salespersons and the products they have sold.

The following facts help to define the structure of the database:

- each salesperson works in a particular shop
- each salesperson has a unique first name
- each shop has one or more salespersons
- each product which is sold is manufactured by one company only
- each salesperson can sell any of the products
- the number of products that each salesperson has sold is recorded

The table `ShopSales` was the first attempt at designing the database.

FirstName	Shop	ProductName	NoOfProducts	Manufacturer
Nick	TX	television set	3	SKC
		refrigerator	2	WP
		digital camera	6	HKC
Sean	BH	hair dryer	1	WG
		electric shaver	8	BG
John	TX	television set	2	SKC
		mobile phone	8	ARC
		digital camera	4	HKC
		toaster	3	GK

(a) State why the table is **not** in First Normal Form (1NF).

.....

.....[1]

(b) The database design is changed to:

SalesPerson (FirstName, Shop)

SalesProducts (FirstName, ProductName, NoOfProducts, Manufacturer)

Using the data given in the first attempt table (ShopSales), show how these data are now stored in the revised table designs.

Table: SalesPerson

FirstName	Shop

Table: SalesProducts

FirstName	ProductName	NoOfProducts	Manufacturer

[3]

(c) (i) A relationship between the two tables has been implemented.

Explain how this has been done.

.....
.....
.....
.....
..... [2]

(ii) Explain why the `SalesProducts` table is **not** in Third Normal Form (3NF).

.....
.....
.....
..... [2]

(iii) Write the table definitions to give the database in 3NF.

.....
.....
.....
..... [2]

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