GCE

Electronics

OCR Advanced Subsidiary GCE H465 Unit F613

Coursework Cover Sheet

Please read the instructions printed overleaf before completing this form. One of these cover sheets, suitably completed, should be attached to the assessed work of **each** candidate in the moderation sample. Please ensure that the appropriate boxes are completed and that the form is signed and dated.

| **Examination session** | June | | **Year** | 2 | 0 | | |

| **Centre name** | |

| **Centre number** | | | | | |

| **Candidate name** | | **Candidate number** | | | | |

Use the grids overleaf, or your own mark record, to calculate the candidate's score for each Assessment Objective. Enter the mark awarded for each Assessment Objective in the appropriate box below. Add together the marks for all the Assessment Criteria to give a total out of 80. Enter this total in the relevant box.

Assessment Criterion	**Mark**
Digital Circuit – Introduction **(max 7)**	
Digital Circuit – Circuit **(max 4)**	
Digital Circuit – Testing **(max 8)**	
Digital Circuit – Report **(max 8)**	
Analogue Circuit – Introduction **(max 7)**	
Analogue Circuit – Circuit **(max 4)**	
Analogue Circuit – Testing **(max 8)**	
Analogue Circuit – Report **(max 8)**	
Microcontroller Circuit – Introduction **(max 8)**	
Microcontroller Circuit – Circuit **(max 4)**	
Microcontroller Circuit – Testing **(max 8)**	
Microcontroller Circuit – Report **(max 6)**	
Total (max 80)	

Digital Circuit

Section	Descriptor	Marks	Centre Mark
1 Introduction			
A	The report does not contain a description of the circuit.	0	
	The report gives a possible use of the circuit and describes circuit operation at a quantitative level.	1–2	
	The report describes circuit operation at a qualitative/quantitative level and makes detailed predictions of expected outputs for a comprehensive set of inputs.	3–4	
B	The report identifies a basic test procedure that will produce at least one set of input/output data.	0–1	
	The report identifies a test procedure that will produce data to support detailed predictions made in 1a (three marks only available if four marks have been awarded for 1a).	2–3	

Section	Descriptor	Marks	Centre Mark
2 Circuit			
A	The circuit build is haphazard and untidy.	0	
	The circuit has been built correctly and is very neat.	1–2	
	The circuit build is very neat and tidy, using an identifiable colour code and meticulous wiring.	3–4	

Section	Descriptor	Marks	Centre Mark
3 Testing			
A	The report contains evidence of a basic test procedure that has produced a single set of input/output data that supports circuit operation at a qualitative level.	0–2	
	The report contains evidence of a thorough testing programme that supports the predictions made of expected outputs for a comprehensive set of inputs.	3	
B	The data of any basic test procedure has been presented in table or graph form.	0–1	
	The data to support detailed circuit predictions made in 1a has been presented in table or graph form (only available if three marks have been awarded for 3a).	2	
C	One or more sets of input/output data have been analysed to support circuit operation.	0–1	
	Two or more sets of input/output have been analysed and a detailed analysis of the predictions made in 1a has been carried out (three marks only available if four marks have been awarded for 1a).	2–3	

Section	Descriptor	Marks	Centre Mark
4 Report			
A	The report contains a correct circuit diagram.	0–1	
	The report also contains labelled diagrams that support the written communication and also clearly show how the testing was performed.	2–3	
B	The report is easy to read and understand.	0–1	
	The report makes good use of spelling and grammar throughout and the report is logically structured with appropriate headings.	2–3	
C	The terminology used in the report has been employed inappropriately.	0	
	The report contains good use of terminology all of the time.	1–2	

Analogue Circuit

Section	Descriptor	Marks	Centre Mark
1 Introduction			
A	The report does not contain a description of the circuit.	0	
	The report gives a possible use of the circuit and describes circuit operation at a quantitative level.	1–2	
	The report describes circuit operation at a qualitative/quantitative level and makes detailed predictions of expected outputs for a comprehensive set of inputs.	3–4	
B	The report identifies a basic test procedure that will produce at least one set of input/output data.	0–1	
	The report identifies a test procedure that will produce data to support detailed predictions made in 1a (three marks only available if four marks have been awarded for 1a).	2–3	

Section	Descriptor	Marks	Centre Mark
2 Circuit			
A	The circuit build is haphazard and untidy.	0	
	The circuit has been built correctly and is very neat.	1–2	
	The circuit build is very neat and tidy, using an identifiable colour code and meticulous wiring.	3–4	

Section	Descriptor	Marks	Centre Mark
3 Testing			
A	The report contains evidence of a basic test procedure that has produced a single set of input/output data that supports circuit operation at a qualitative level.	0–2	
	The report contains evidence of a thorough testing programme that supports the predictions made of expected outputs for a comprehensive set of inputs.	3	
B	The data of any basic test procedure has been presented in table or graph form.	0–1	
	The data to support detailed circuit predictions made in 1a has been presented in table or graph form (only available if three marks have been awarded for 3a).	2	
C	One or more sets of input/output data have been analysed to support circuit operation.	0–1	
	Two or more sets of input/output have been analysed and a detailed analysis of the predictions made in 1a has been carried out (three marks only available if four marks have been awarded for 1a).	2–3	

Section	Descriptor	Marks	Centre Mark
4 Report			
A	The report contains a correct circuit diagram.	0–1	
	The report also contains labelled diagrams that support the written communication and also clearly show how the testing was performed.	2–3	
B	The report is easy to read and understand.	0–1	
	The report makes good use of spelling and grammar throughout and the report is logically structured with appropriate headings.	2–3	
C	The terminology used in the report has been employed inappropriately.	0	
	The report contains good use of terminology all of the time.	1–2	

Microcontroller Circuit

Section		Descriptor	Marks	Centre Mark
1 Introduction				
	A	The report contains a brief statement about circuit operation that includes a possible use of the circuit.	0–1	
		The report contains a correct flowchart and describes circuit operation at a qualitative level.	2–3	
		The report describes circuit operation at a quantitative level and makes detailed predictions of expected outputs for a comprehensive set of inputs.	4–5	
	B	The report does not contain any reference to the testing procedure.	0	
		The report identifies a basic test procedure that will produce at least one set of input/output data and a description of how the basic test procedure is to be carried out.	1–2	
		The report identifies a detailed test procedure that will produce data to support detailed predictions made in 1a (only available if five marks have been awarded for 1a).	3	

Section		Descriptor	Marks	Centre Mark
2 Circuit				
	A	The circuit build does not merit a mark.	0	
		The circuit has been built correctly and is very neat.	1–2	
		The circuit build is very neat and tidy, using an identifiable colour code and meticulous wiring.	3–4	

Section		Descriptor	Marks	Centre Mark
3 Testing				
	A	The report contains evidence of a basic test procedure that has produced a single set of input/output data that supports circuit operation at a qualitative level.	0–1	
		The report contains evidence of a testing programme that produced a single set of input/output data and supports circuit operation at a quantitative level and predictions made of expected outputs for a comprehensive set of inputs.	2–3	
	B	The presentation of results does not merit a mark.	0	
		The data to support detailed circuit predictions made in 1a has been presented in an appropriate manner (two marks only available if three marks have been awarded for 3a).	1–2	
	C	At least one set of input/output data has been analysed to support circuit operation.	0–1	
		Two or more sets of input/output data have been analysed to support circuit operation and a detailed analysis of the predictions made in 1a has been carried out (three marks only available if five marks have been awarded for 1a).	2–3	

Section		Descriptor	Marks	Centre Mark
4 Report				
	A	No diagrams have been included in the report or the diagrams presented do not merit a mark.	0	
		The report contains a correct circuit diagram, contains labelled diagrams that support the written communication and clearly shows how the testing was performed.	1–2	
	B	The report is easy to read and understand.	0–1	
		The report makes good use of spelling and grammar throughout and the report is logically structured with appropriate headings.	2–3	
	C	The report contains good use of terminology.	0-1	