

Electronics

Advanced GCE F615

Communications Systems

Mark Scheme for June 2010

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Mark schemes should be read in conjunction with the published question papers and the Report on the Examination.

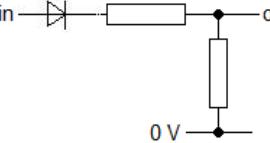
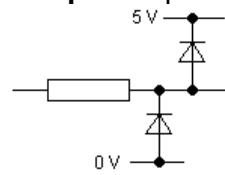
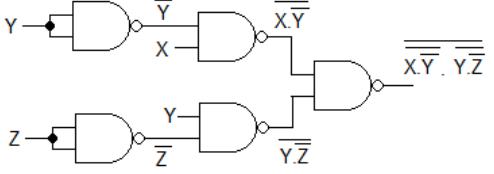
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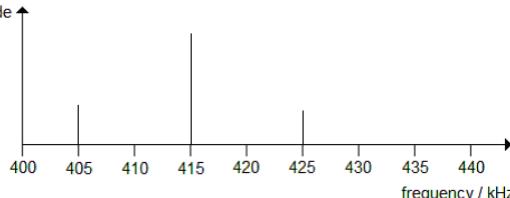
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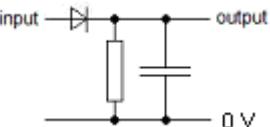
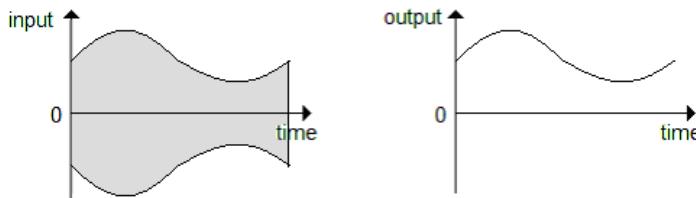
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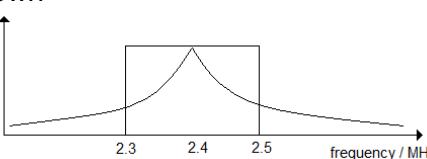
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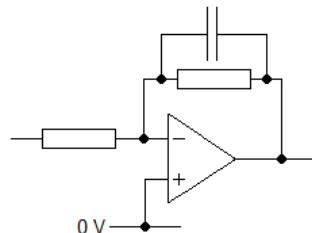
Question	Grade	Expected Answer	Mark	Additional Guidance
1 (a)	E E	resistor chain between supply rails junctions connected to 3 V, 2 V and 1 V	1 1	
1 (b)	E E A	voltage divider to reduce voltage diode to cut out negative signals resistors in correct ratio 15:10 (accept 16:10)	1 1 1	<p>ignore size of resistors accept Zener diode and series resistor for [2]:</p>  <p>accept clamp diodes and series resistor for [1]</p> 
1 (c) (i)	D D	Second row is only time Y is 1 and Z is 0 Third row only time that X is 1 and Y is 0	1 1	
1 (c) (ii)	D C B A	NAND gate to generate output NAND gates as NOT gates correct circuit correct algebra at inputs and outputs of all gates	1 1 1 1	<p>both inputs joined or one held high accept pairs of redundant NOT gates</p> 
1 (c) (iii)	C	B = Y	1	

Question	Grade	Expected Answer	Mark	Additional Guidance
1 (d)	C E A E	range is difference between largest and smallest voltages which can be coded 3 V resolution is voltage difference between adjacent codes 1 V	1 1 1 1	accept below 1V to above 3V
2 (a) (i)	D C	amplitude of (carrier) at S determined by voltage of signal at M	1 1	
2 (a) (ii)	C B	spike at 415 kHz smaller same-height spikes at 405 kHz and 425 kHz	1 1	
				
2 (b)	B A	8.08 kΩ (accept 8.0 or 8.1) EITHER $I = (15 - 2.2) / 47 \times 10^3 = 2.72 \times 10^{-4} \text{ A}$ $R = 2.2 / 2.72 \times 10^{-4} \dots$ OR $(15-2.2) / 2.2 = 47k/R_2 \dots$ OR $2.2 = 15 \times \frac{R_2}{47 + R_2} \dots$	2	correct answer for [2] otherwise look for correct method for [1]
2 (c)	D C B A	op-amp set up as amplifier for S; gain depends on resistance of MOSFET; which is determined by voltage at gate; so gain of amplifier altered by M;	1 1 1 1	

Question	Grade	Expected Answer	Mark	Additional Guidance
2 (d) (i)	E D	correct symbols for resistor, capacitor, diode circuit as shown below	1 1	
				
2 (d) (ii)	E D C B	diode rectifies signal / removes negative part of signal RC network filters out high frequency components sketch of AM signal at input sketch of matching demodulated signal at output	1 1 1 1	accept smoothes signal accept output signal centred on zero
				
3 (a)	E	$(108 - 87.5)/0.2 = 102.5$ or 102	1	accept reverse calculation: 100 stations gives 205 kHz
3 (b) (i)	E D	FM bandwidth is $5f_s$, AM bandwidth is $2f_s = 80$ kHz channels = $(108 - 87.5)/0.08 = 256$	1 1	ecf incorrect AM bandwidth between 10 and 100 kHz
3 (b) (ii)	C B A E	noise added to signal in transmission can be removed from FM by Schmitt trigger / limiter but not possible for AM	1 1 1 1	accept alternative answer: <ul style="list-style-type: none">• AM codes with amplitude [1]• FM codes with frequency [1]• noise affect amplitude more than frequency [1]
3 (c) (i)	B D A	manipulation of rule: $C = \frac{1}{4\pi^2 L f_0^2}$ μ is 10^{-6} , M is 10^6 , p is 10^{-12} $C = 200$ pF	1 1 1	accept values substituted instead of algebra ecf incorrect units conversion

Question	Grade	Expected Answer	Mark	Additional Guidance
3 (c) (ii)	E E E C B A	R - no change of resistance with frequency C - reactance decreases with increasing frequency L - reactance increases with increasing frequency R usually bigger than L or C so output small except when L, C have the same reactance when their joint resistance greater than R	1 1 1 1 1 1	
3 (d) (i)	E	peak at 2.4 MHz, sloping down above and below, as shown 	1	not two straight lines accept any bandwidth
3 (d) (ii)	E E C	three filters in series with buffer amplifiers between	1 1 1	
3 (d) (iii)	B A	each has slightly different centre frequency buffer amplifiers stop loading from affecting bandwidth	1 1	accept resonant frequency
4 (a)	E D C	PSC takes binary word and feeds out bits one after the other down the link SPC takes bits off the link and puts them on parallel lines to make a binary word DAC outputs a voltage according to the binary word at its inputs.	1 1 1	not just poarallel-to-serial converter not just serial-to-parallel converter not just digital-to-analogue converter
4 (b) (i)	E D	4200 Hz need to sample at least twice in each cycle	1 1	
4 (b) (ii)	D C	informs the SPC that the word is about to arrive	1 1	

Question	Grade	Expected Answer	Mark	Additional Guidance
4 (b) (iii)	A E C	each word is $6+2=8$ bits bit rate = $8400 \times 8 = 67\ 200\ s^{-1}$ bandwidth = $67\ 200 / 2 = 33.6\ kHz$	1 1 1	ecf: 6 bits gives $50\ 400\ s^{-1}$ for [1] and $25\ kHz$ for [1]
4 (c)	A*	packet contains words which codes for <ul style="list-style-type: none">• signal voltage• input signal channel address• output signal channel address• checksum/parity bits each channel allowed access to link many times a second, in turn; decoded signal sent to output channel according to output signal address, via multiplexers;	1 1 1 1 1 1	accept data payload accept source address accept destination address
5 (a)	E E	0 V negative feedback	1 1	not virtual earth accept description of negative feedback
5 (b)	A*	0001 gives -0.2 V out $I = 0.2/27 \times 10^3 = 7.41 \times 10^{-6}\ A$ in feedback resistor $R_A = 5 / 7.41 \times 10^{-6} = 675\ k\Omega$ $R_B = 338\ k\Omega, R_C = 169\ k\Omega, R_D = 84.4\ k\Omega$	1 1 1 1	accept $680\ k\Omega, 340\ k\Omega, 170\ k\Omega$ and $85\ k\Omega$ ecf incorrect $R_A: R_A = 2R_B = 4R_C = 8R_D$ for [1]
5 (c)	C E E D E	correct circuit resistors in range $1\ k\Omega$ to $1\ M\Omega$ feedback resistor 4 times input resistor $RC = 3.8 \times 10^{-5}\ s$ for feedback loop use of break frequency formula	1 1 1 1 1	



Question	Grade	Expected Answer	Mark	Additional Guidance
5 (d) (i)	D C	SI accepts data one bit at a time clock pulses at CK synchronise capture of bits	1 1	accept serial input and clock input for [1]
5 (d) (ii)	E C E E	Q to D of next flip-flop clocks joined to CK first D to SI Q outputs to DCBA or ABCD	1 1 1 1	not Q_n
6 (a)	E E	line sync frame sync	2	correct pattern for [2] one mistake for [1]
6 (b)	E C	intensity determined by voltage / amplitude of red, green and blue signals	1 1	ignore references to colour of pixel
6 (c)	E D D	during (raster) scan of pixels line sync indicates when to move to next line frame sync indicates when to start a new frame	1 1 1	accept description of raster scan
6 (d)	E C A	$\text{pixels per frame} = 512 \times 1024 = 524288$ $\text{pixels per second} = 524288 \times 50 = 26.2 \times 10^6 \text{ s}^{-1}$ ecf: bandwidth = pixel rate / 2 = 13.1 MHz	1 1 1	
7 (a)	D E D E	infrared along optical fibre electrical (signal) / voltage / current along cable / wire / metal / twisted pair	1 1 1 1	accept light
7 (b) (i)	C B	noise is random signal (from components) interference is signal from other electrical systems	1 1	

7 (b) (ii)	E	intensity decreases	1	accept attenuation
7 (c)	E B A	optical fibre any two of the following, [1] each <ul style="list-style-type: none">• no random light in the fibre• stray light can't get in / no reaction to electricity• low absorption / attenuation so long distance	1 2	no ecf for incorrect method

Quality of Written Communication

- 3 The candidate expresses complex ideas extremely clearly and fluently. Sentences and paragraphs follow on from one another smoothly and logically. Arguments are consistently relevant and well structured. There will be few, if any, errors of grammar, punctuation and spelling.
- 2 The candidate expresses straightforward ideas clearly, if not always fluently. Sentences and paragraphs may not always be well connected. Arguments may sometimes stray from the point or be weakly presented. There may be some errors of grammar, punctuation and spelling, but not such as to suggest a weakness in these areas.
- 1 The candidate expresses simple ideas clearly, but may be imprecise and awkward in dealing with complex or subtle concepts. Arguments may be of doubtful relevance or obscurely presented. Errors in grammar, punctuation and spelling may be noticeable and intrusive, suggesting weaknesses in these areas.
- 0 The language has no rewardable features.

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